Compal Confidential

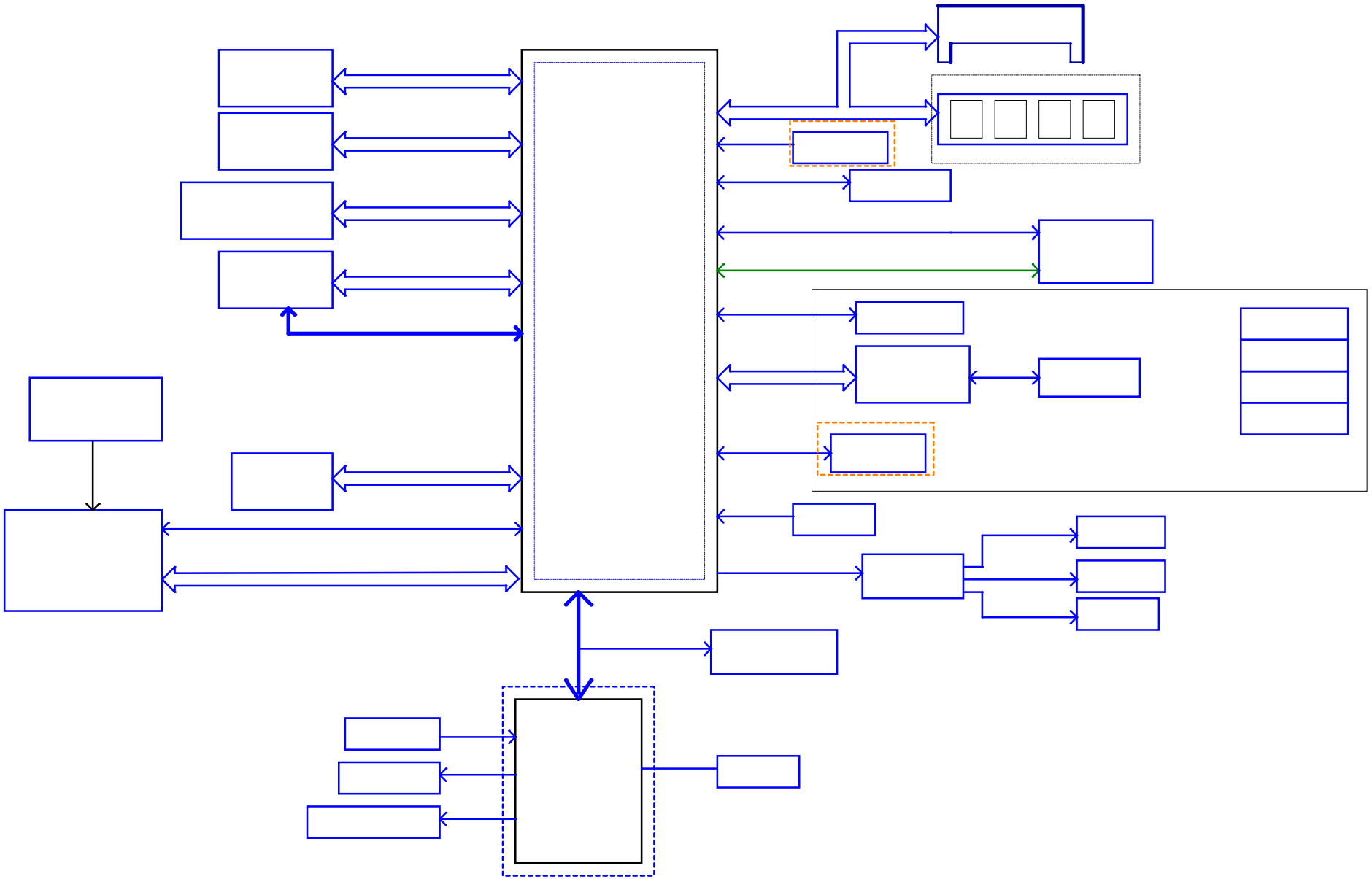
GLS1A_GLS1B

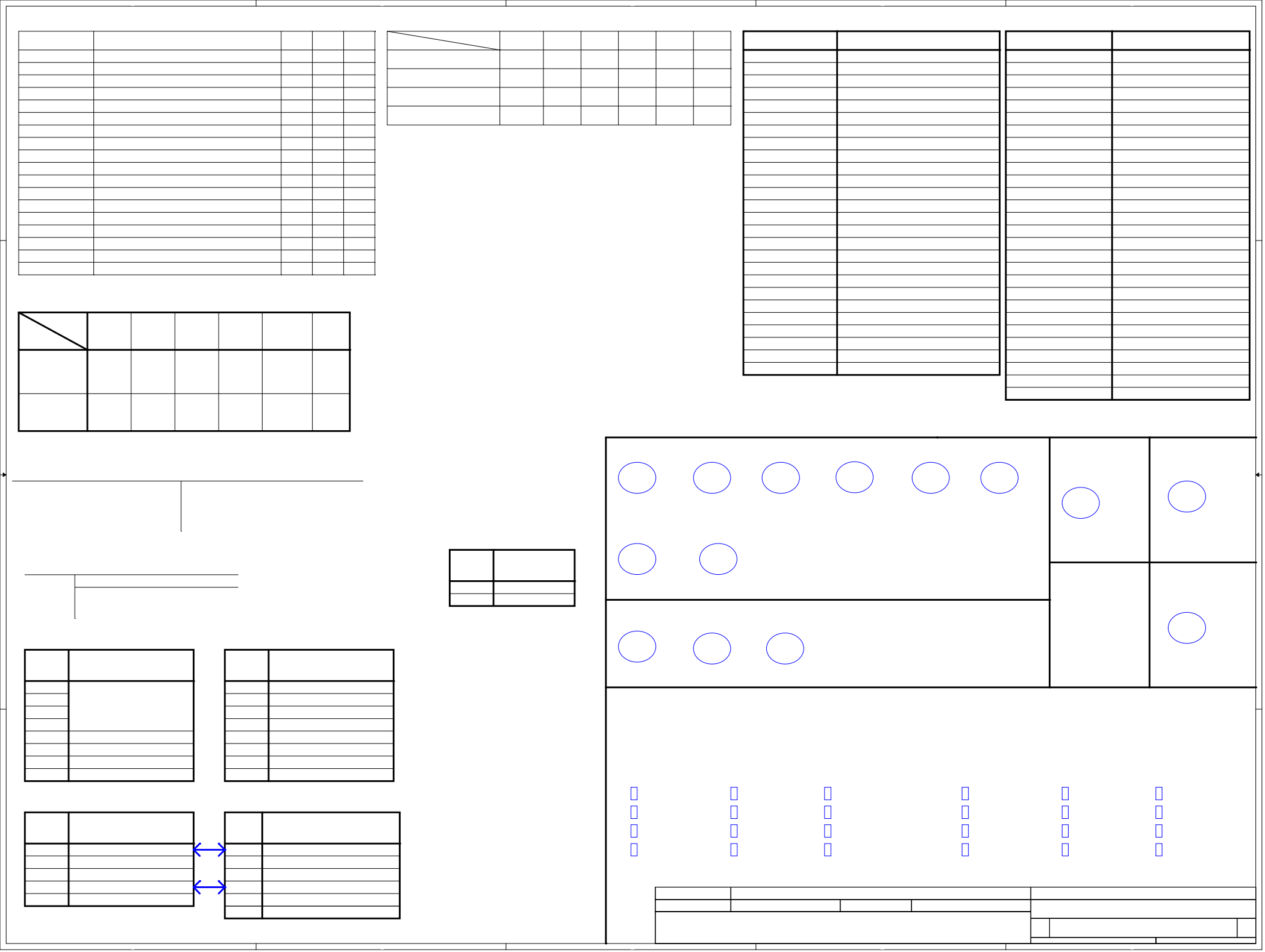
M/B Schematic Documents

LA-L501P

2021-08-06

Rev : 1A





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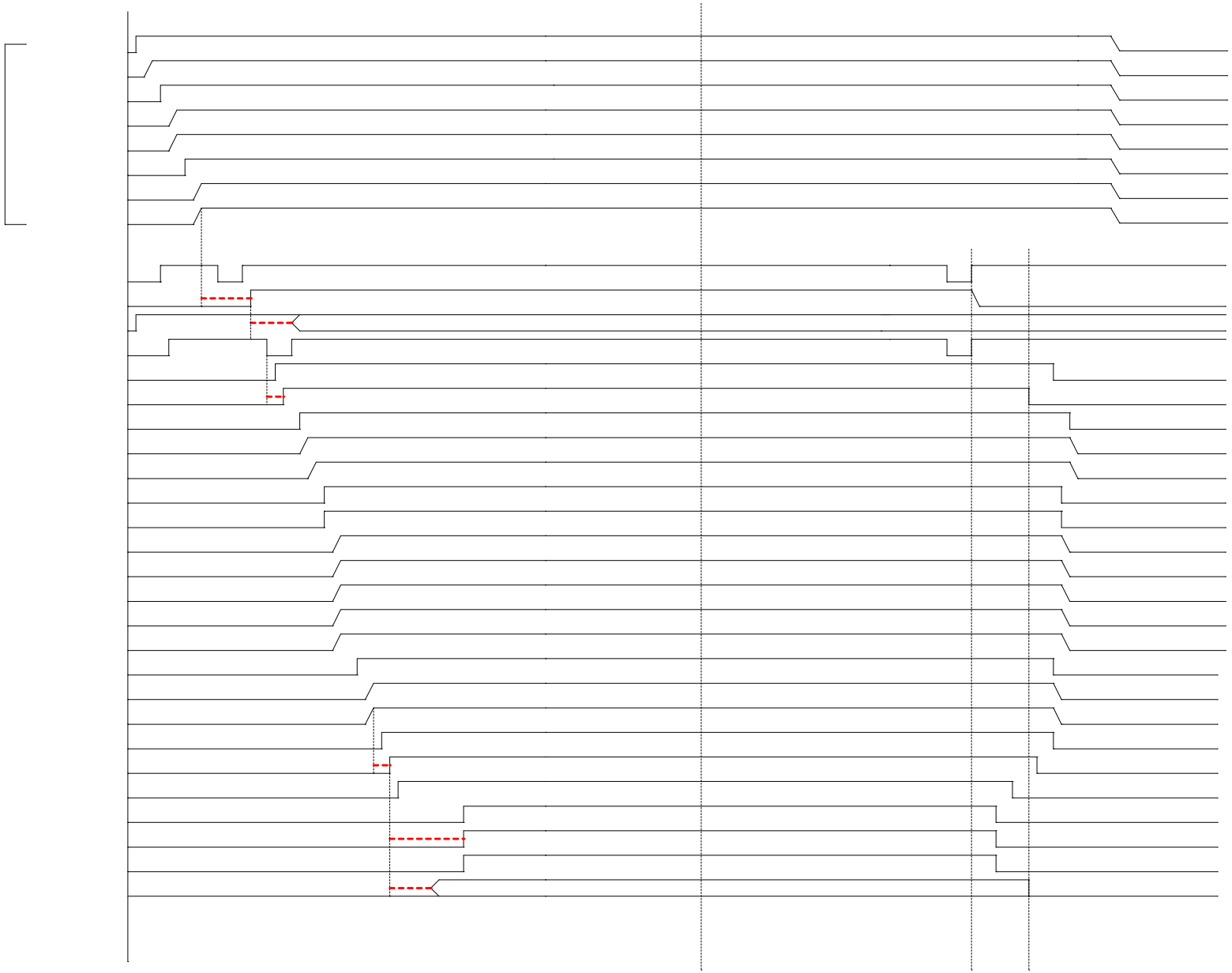

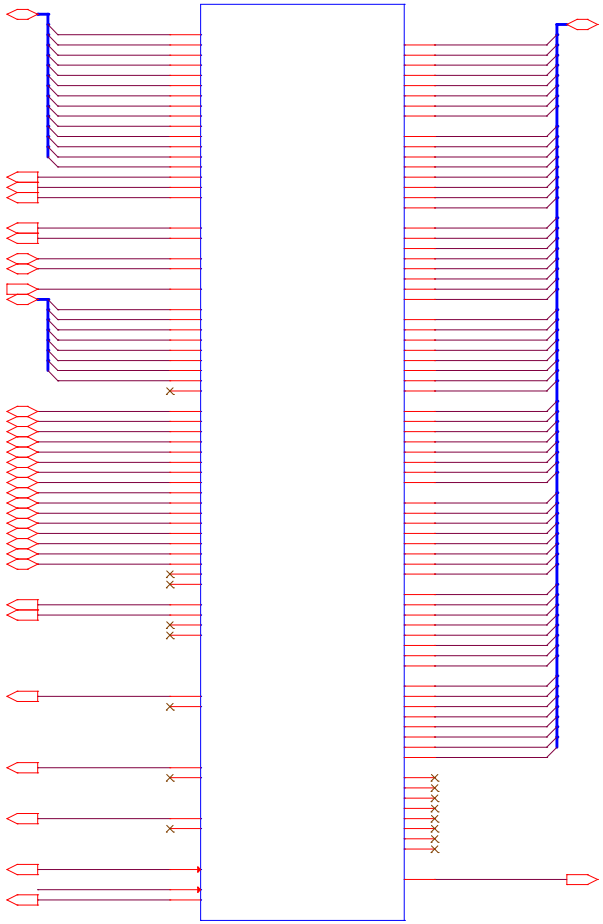

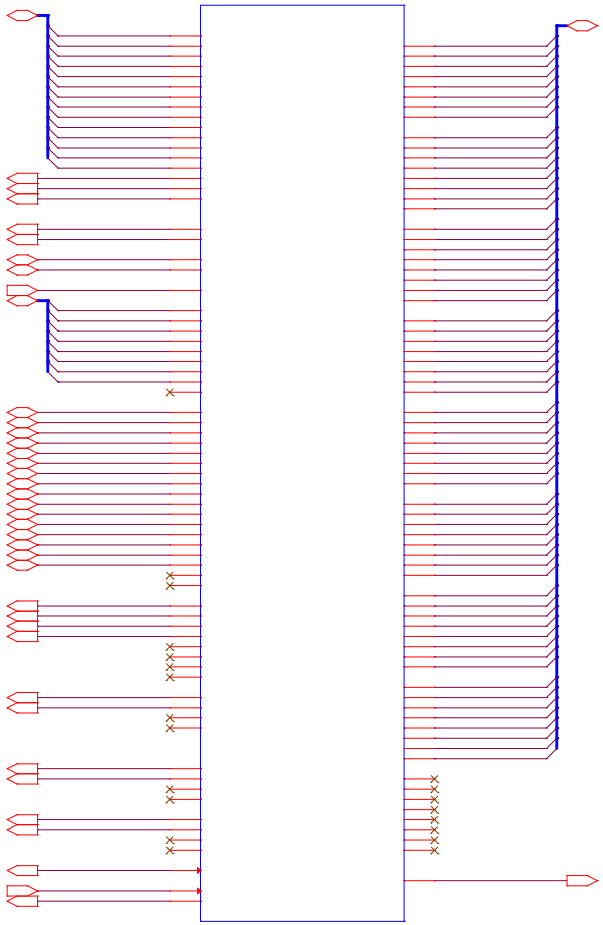

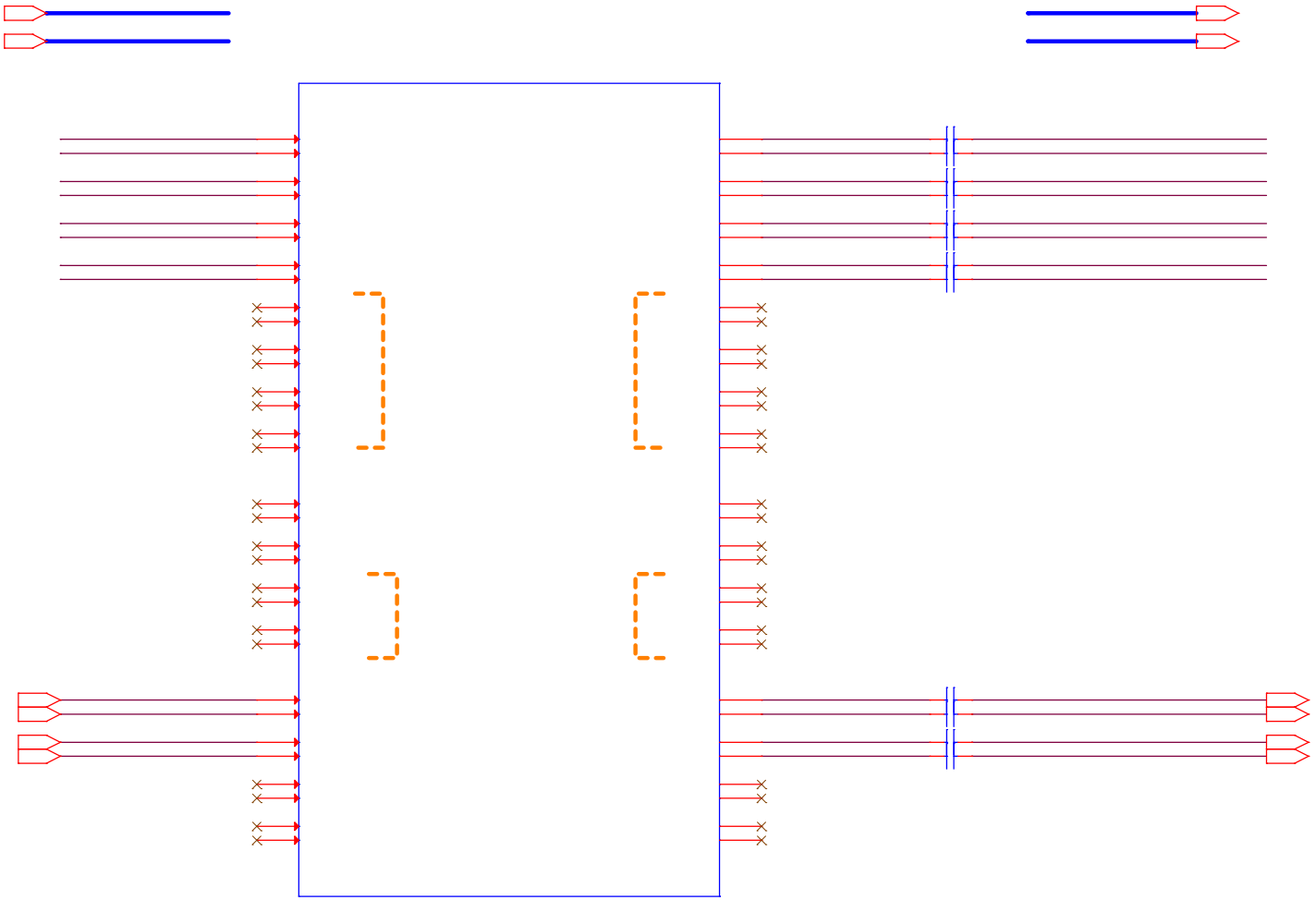

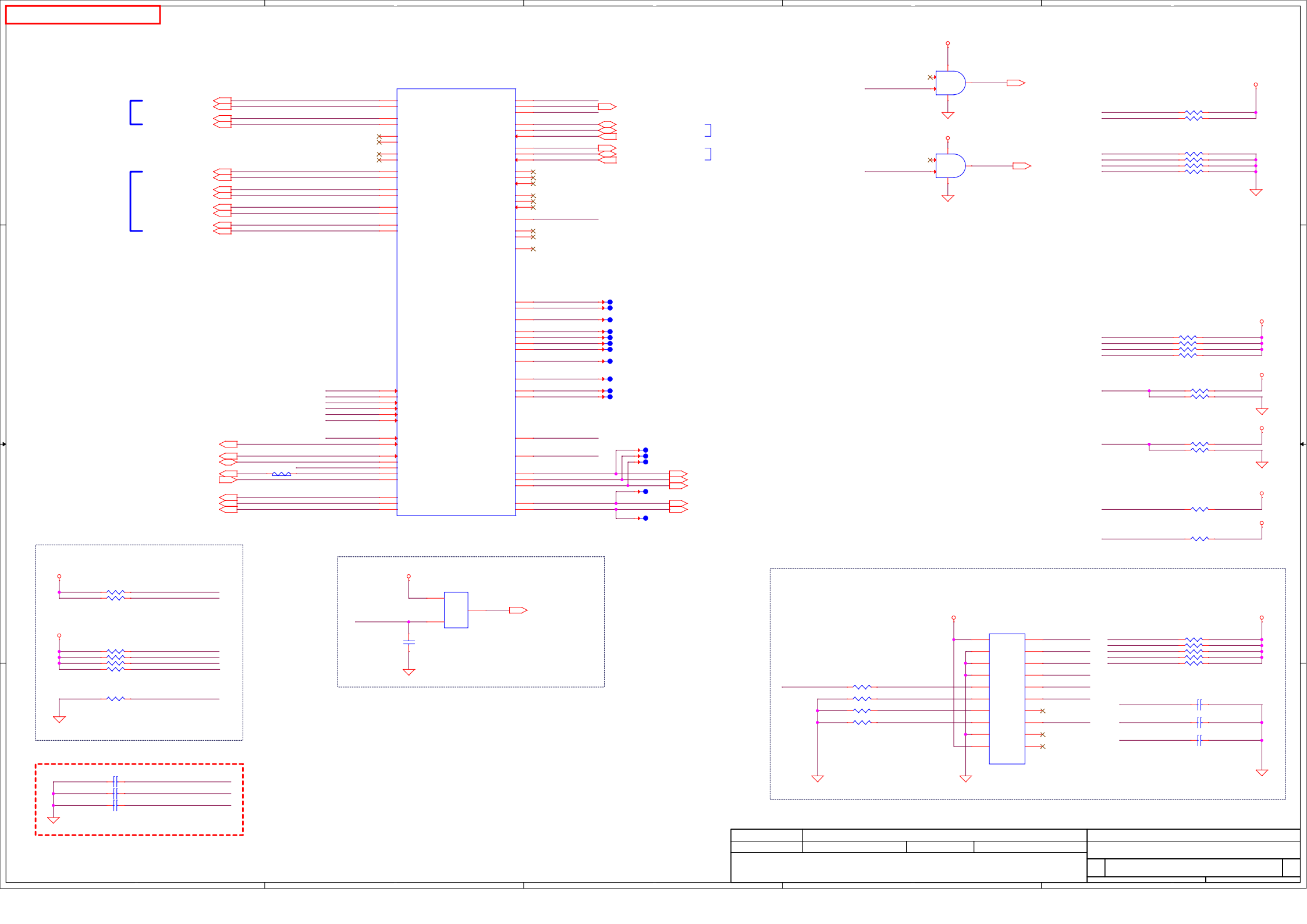

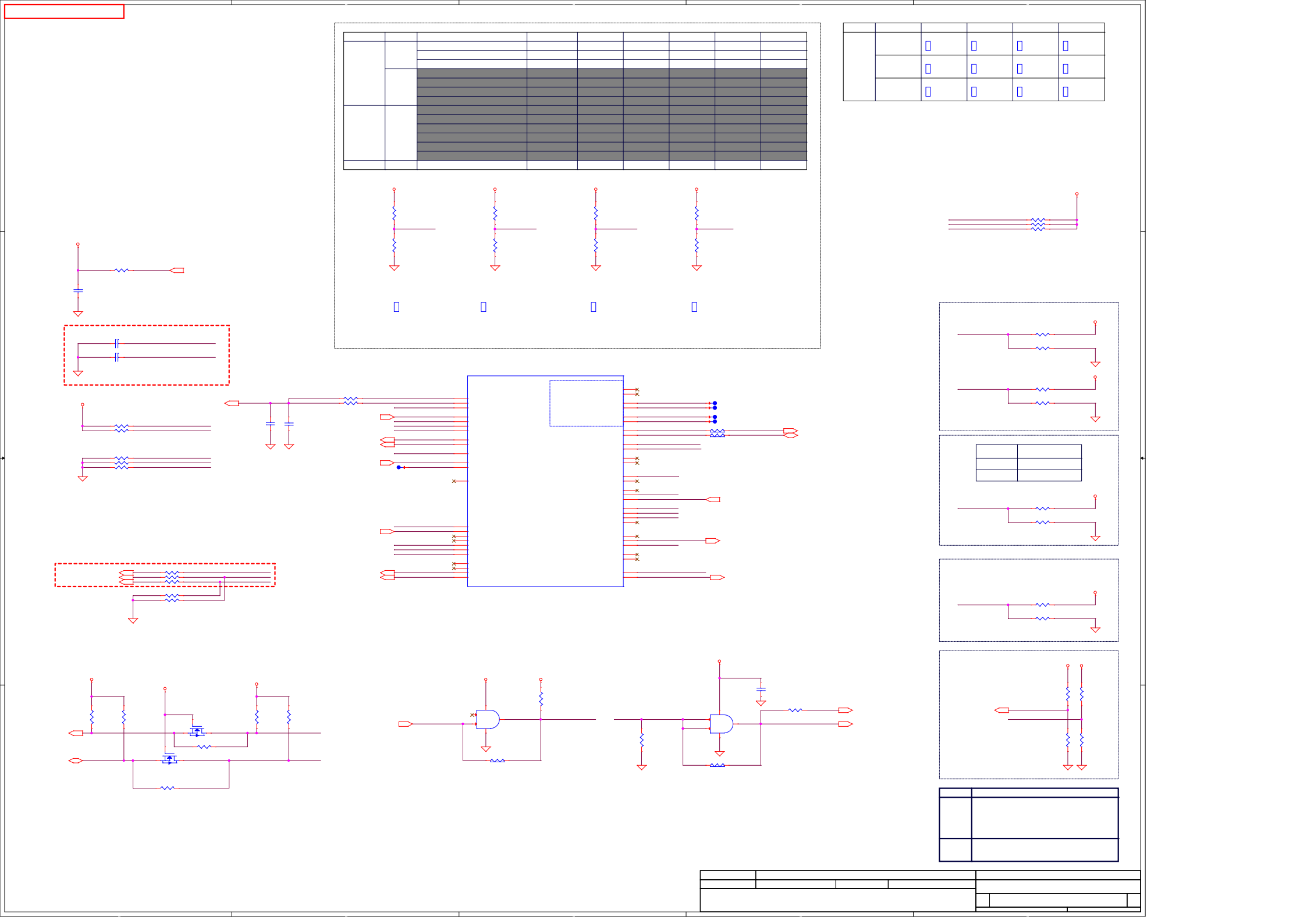

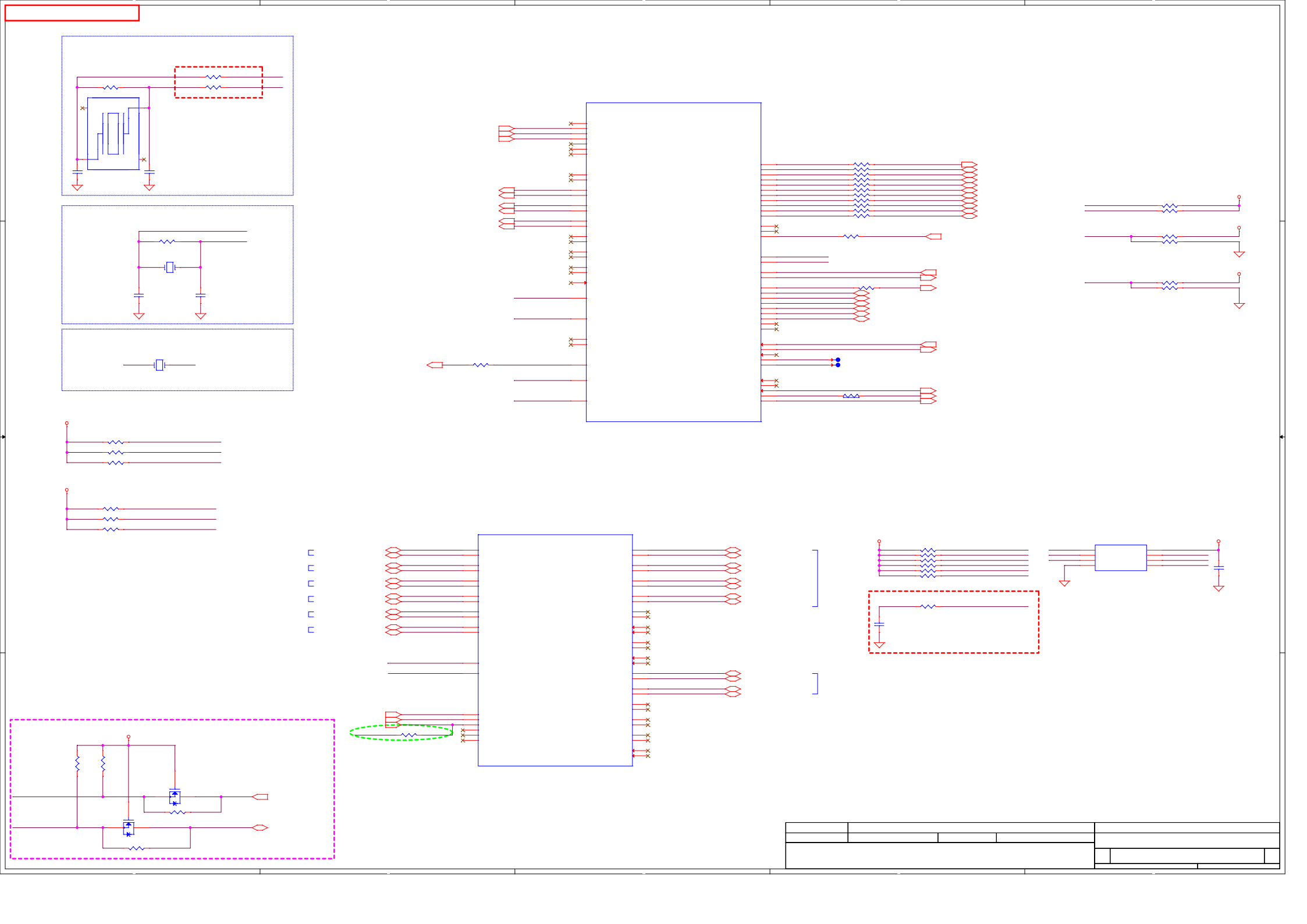

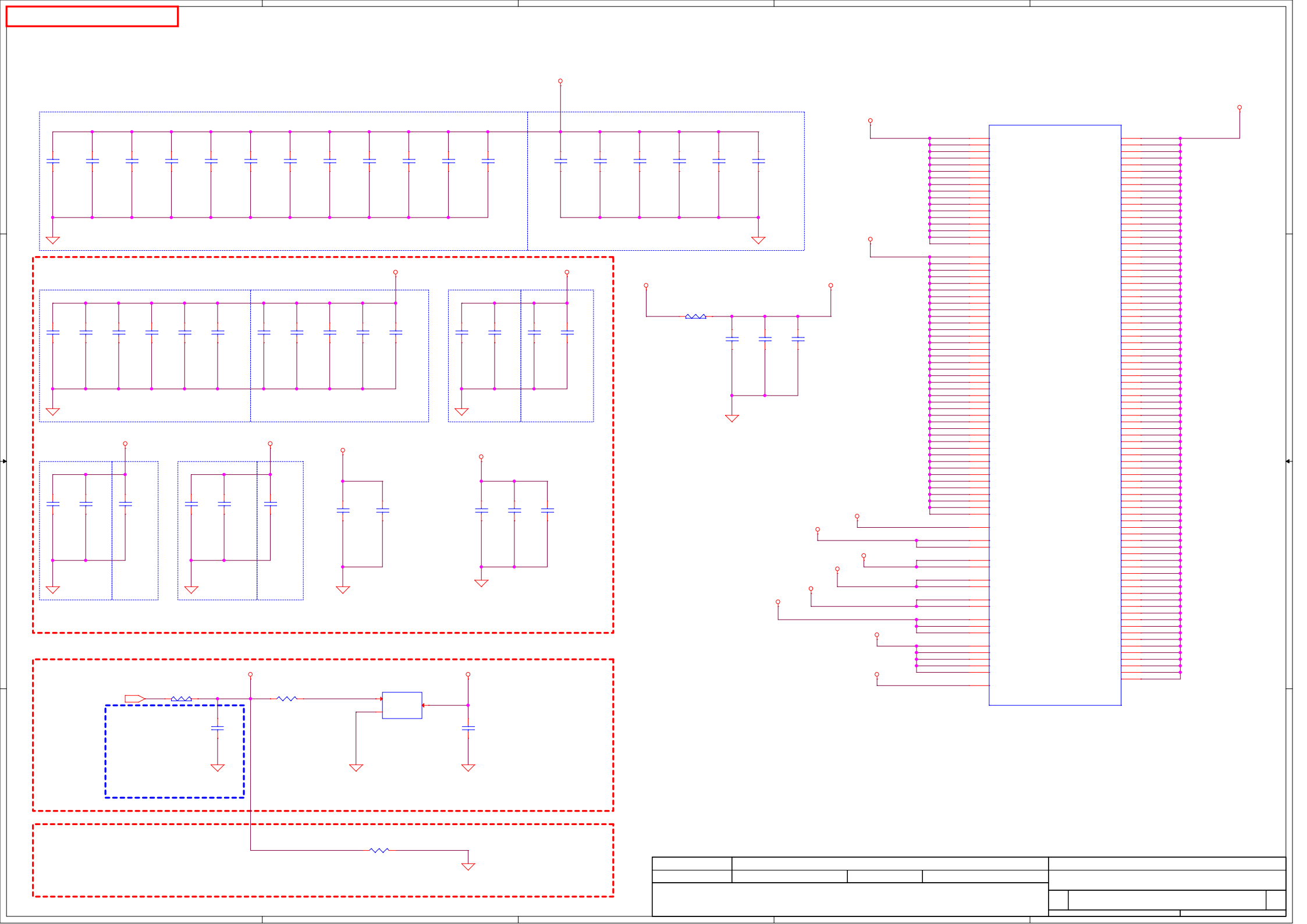

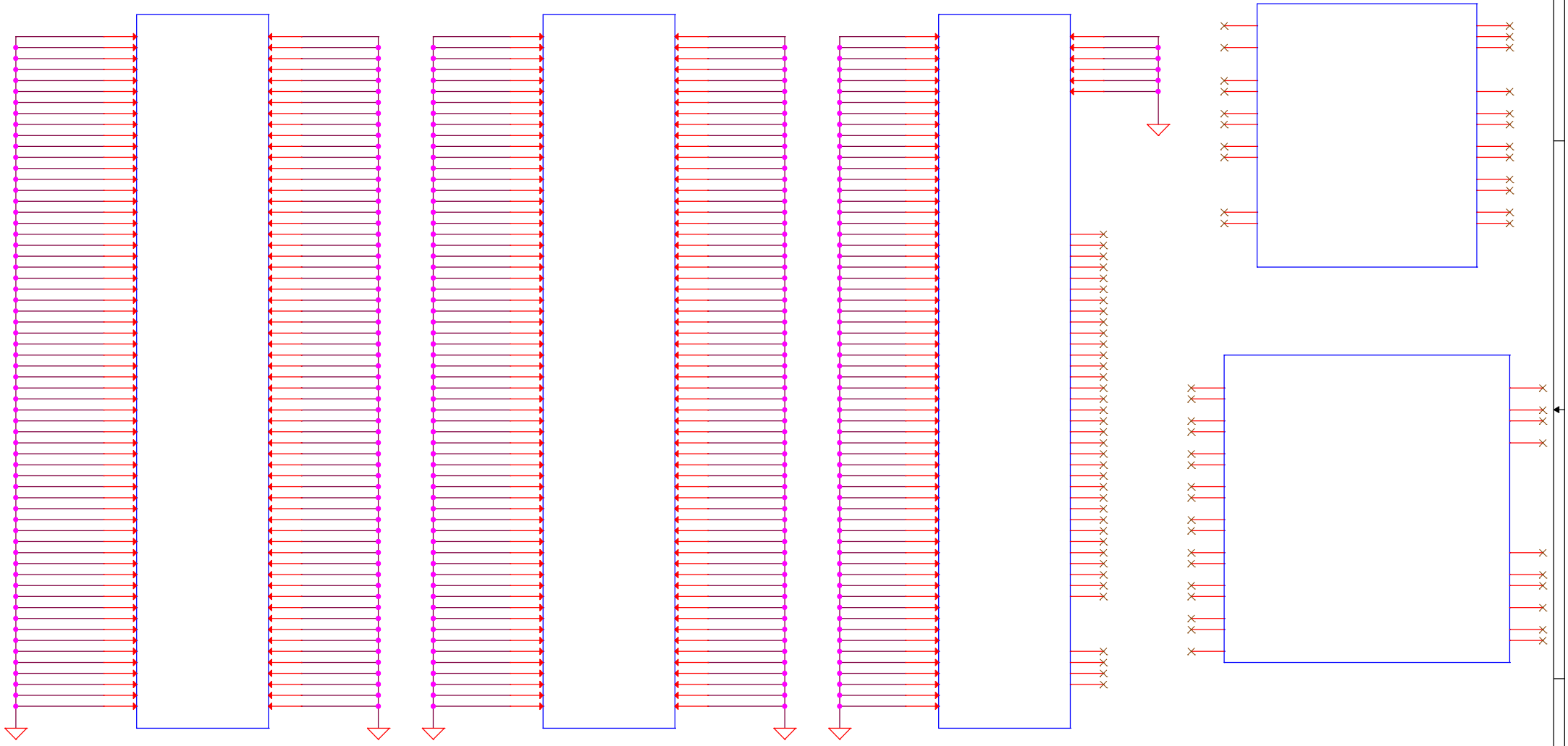

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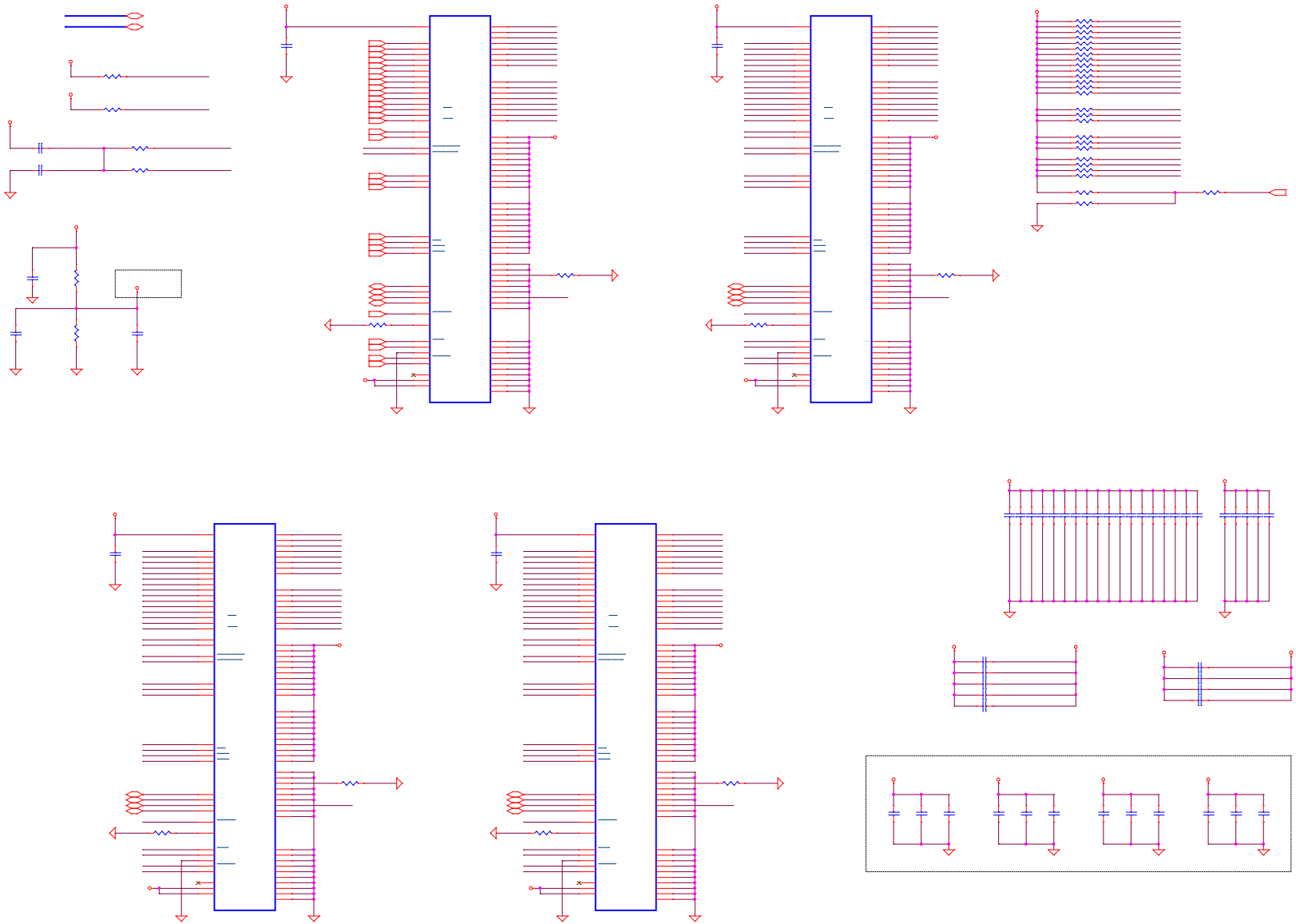

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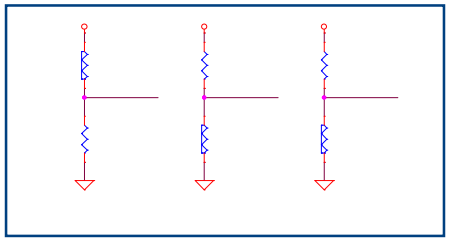

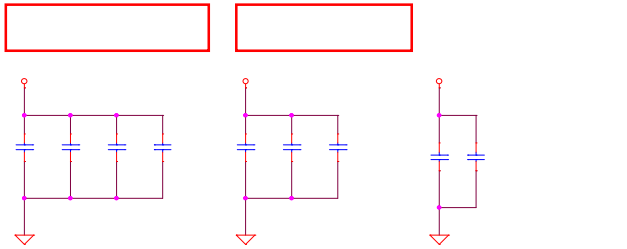

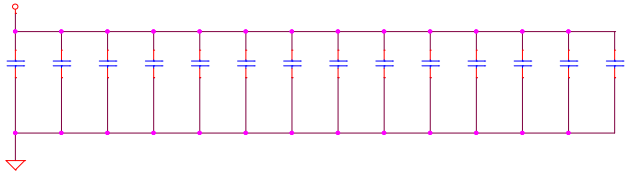

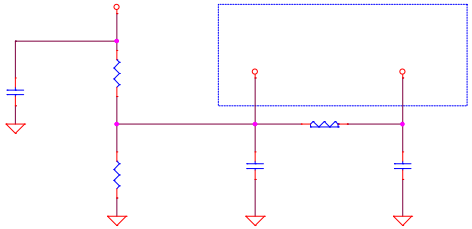

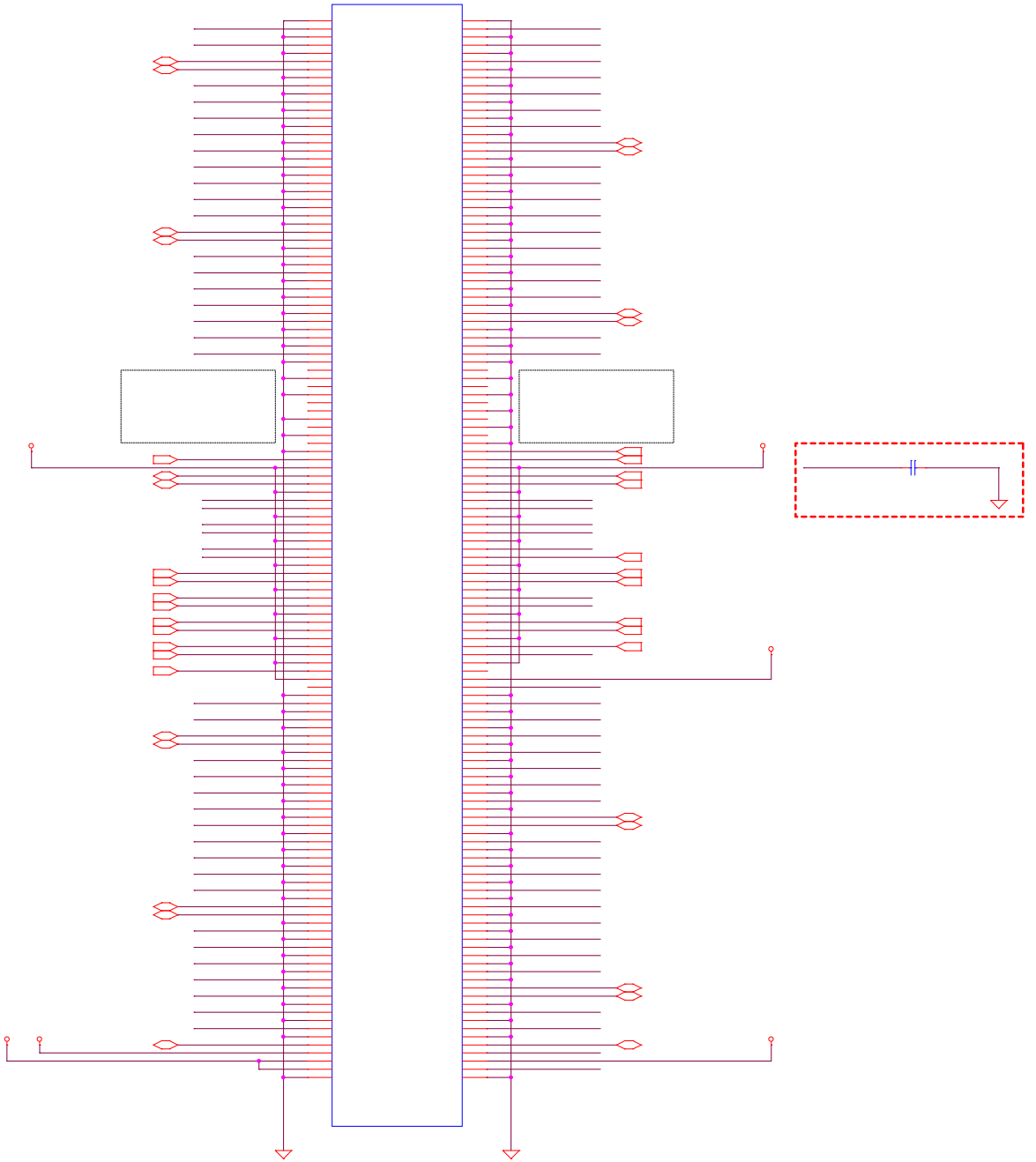

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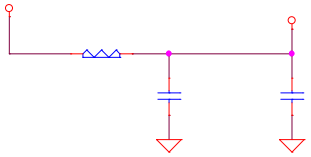
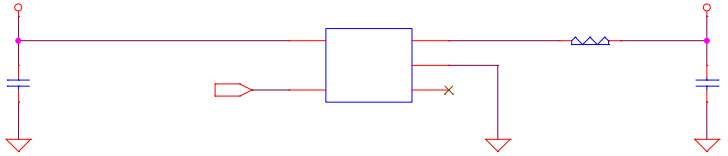
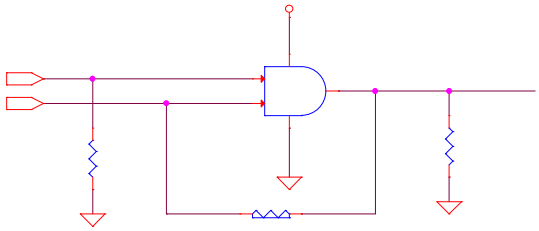
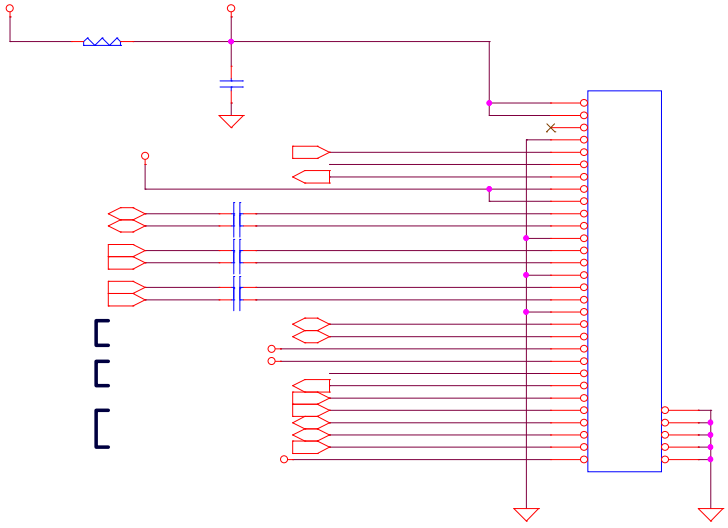
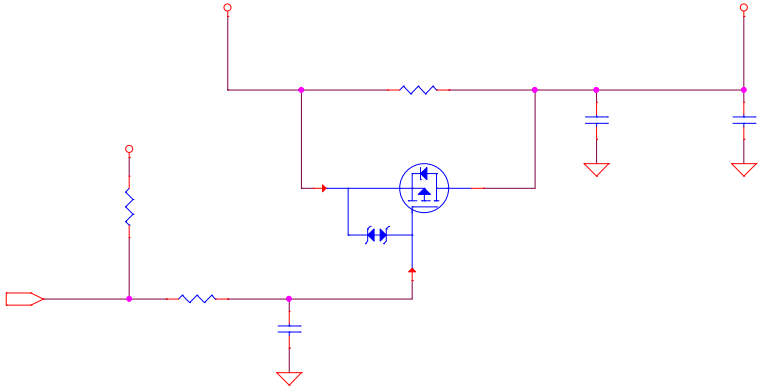


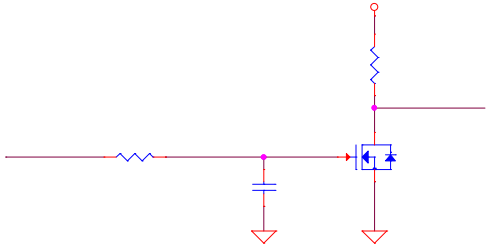


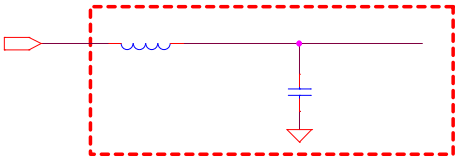


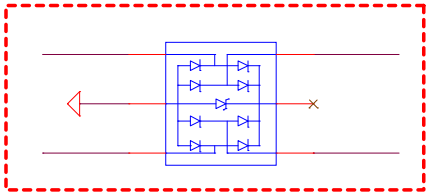
Touch Screen POWER CIRCUIT











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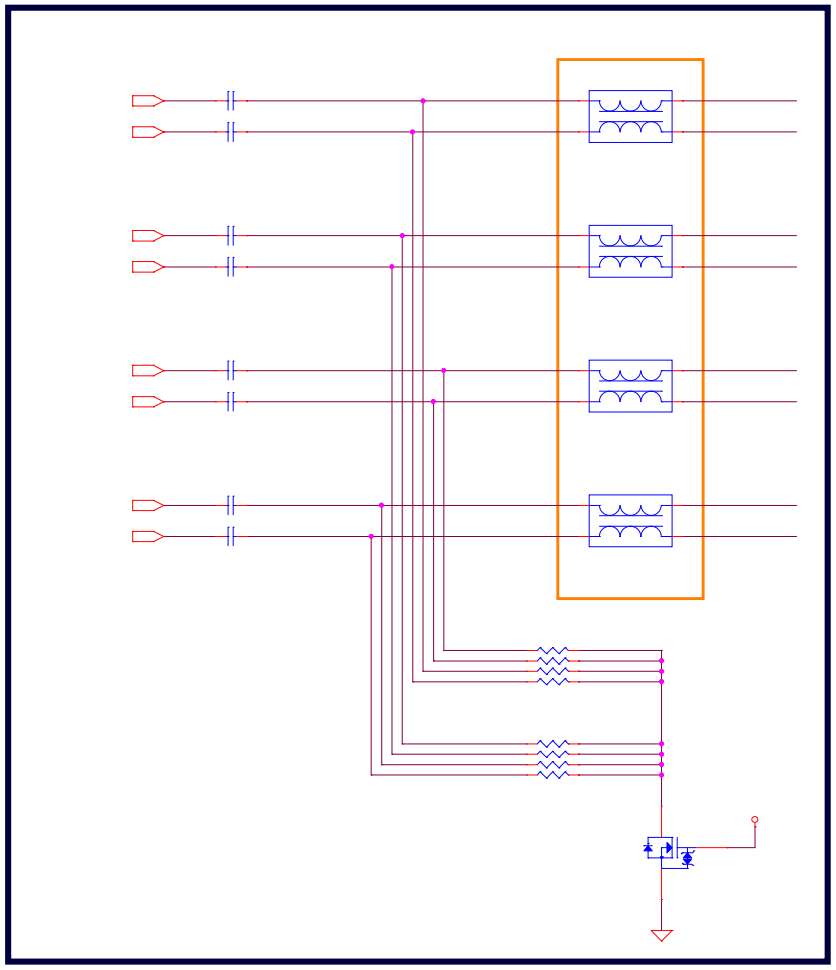

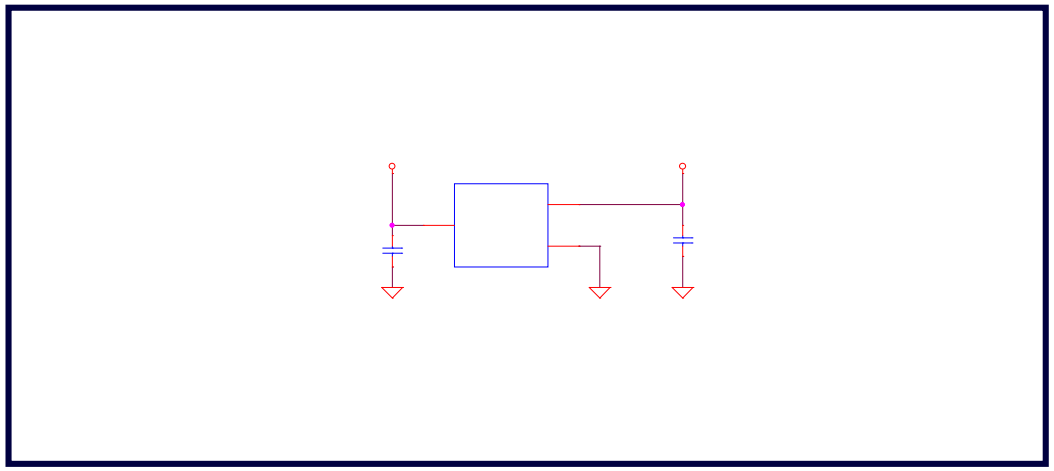

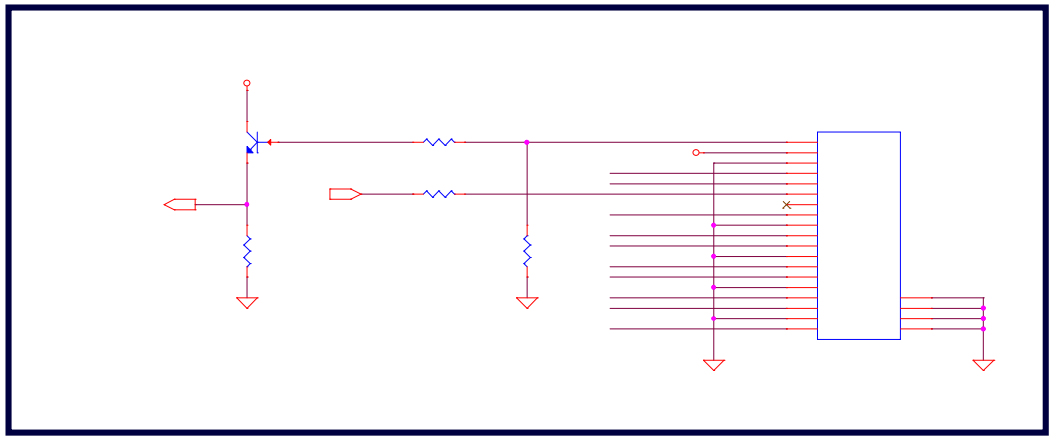

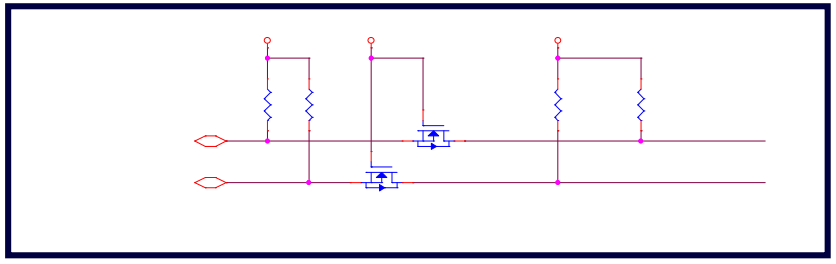

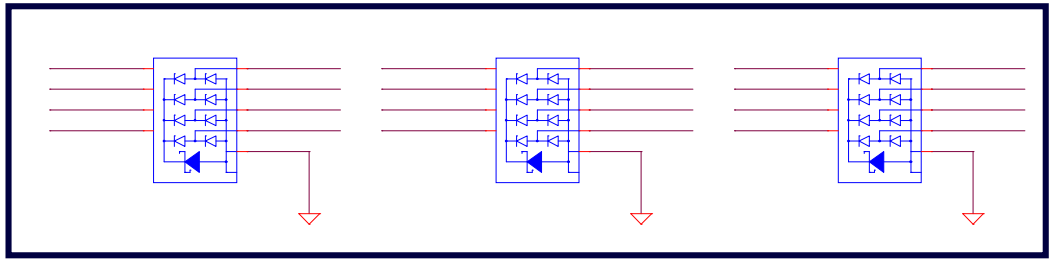

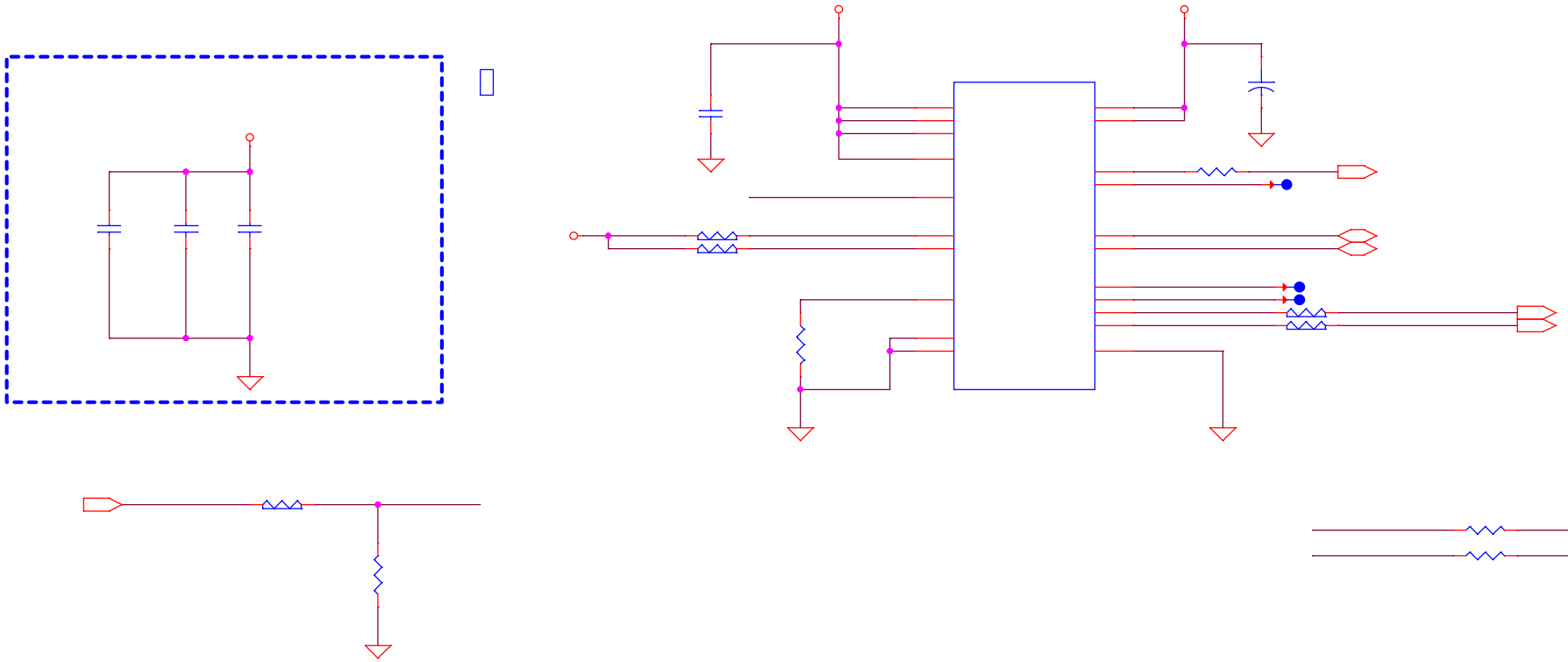

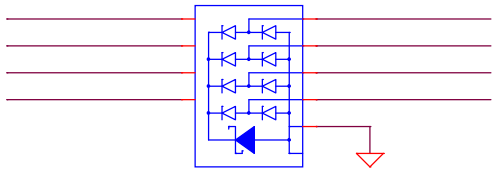

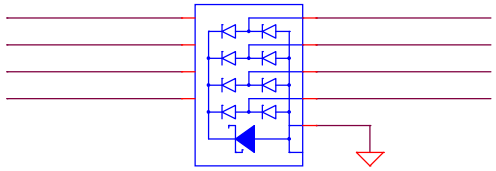

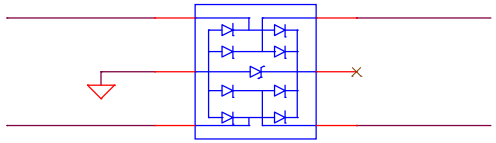

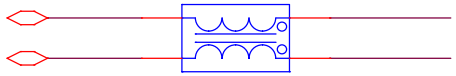

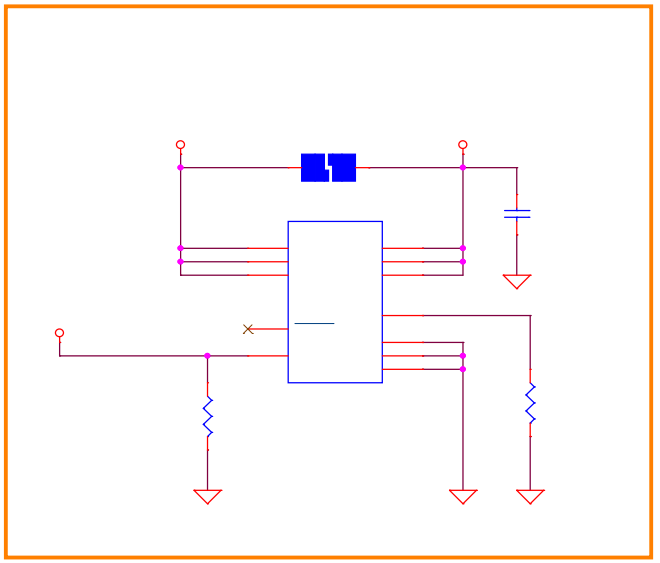

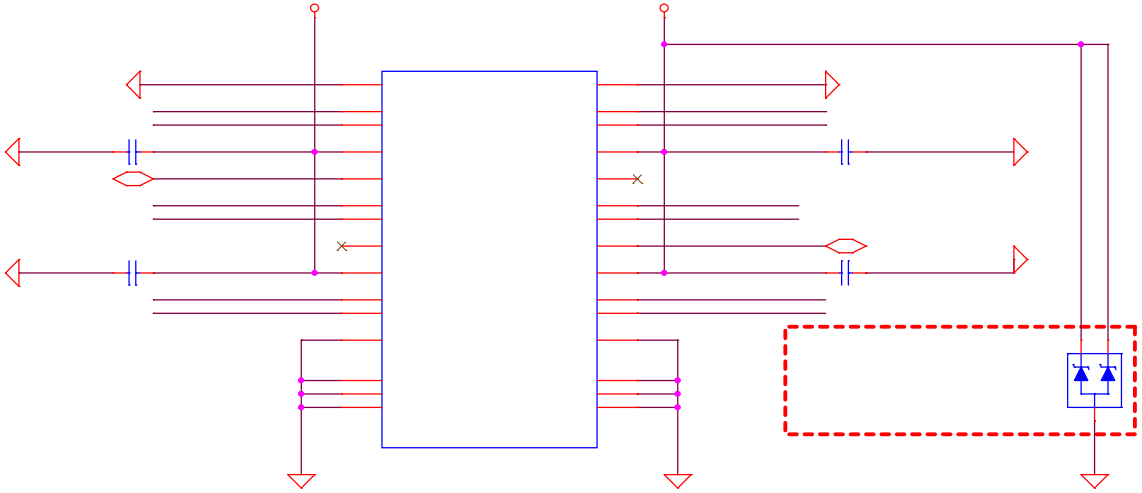

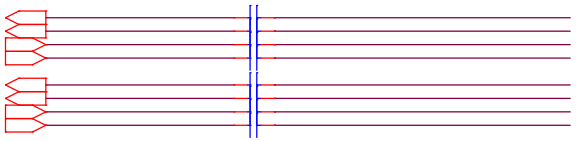

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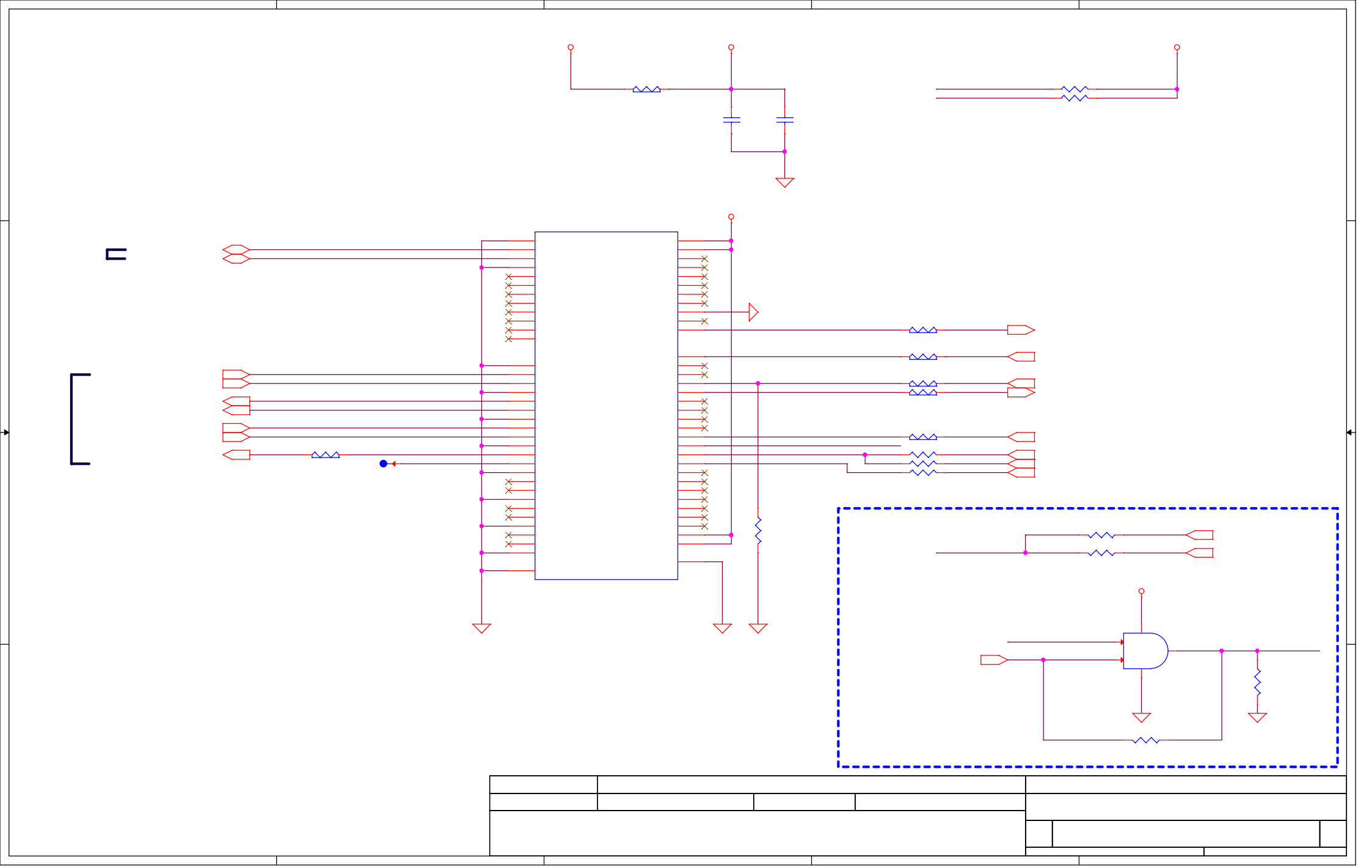

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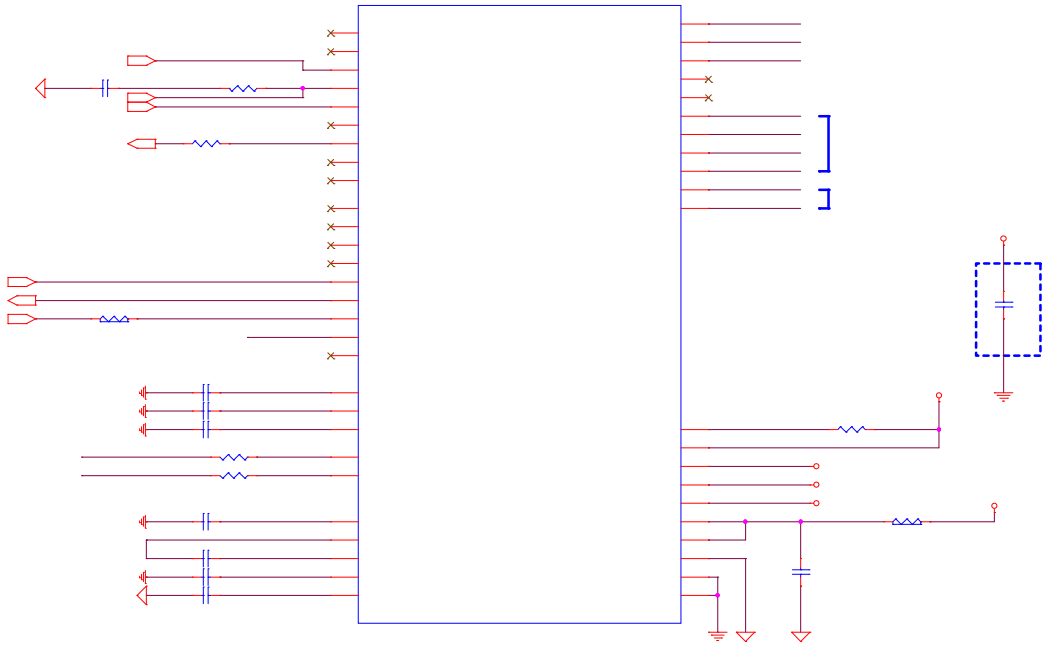

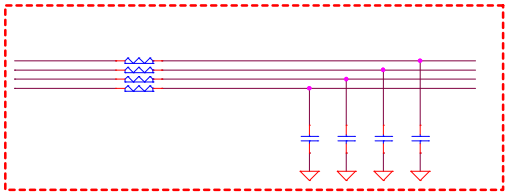

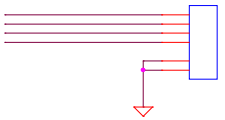

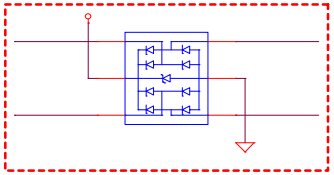

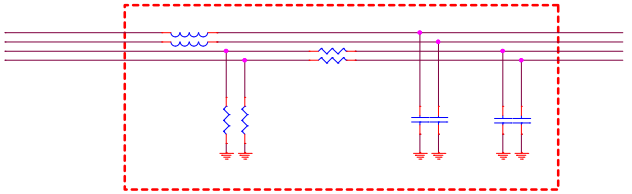

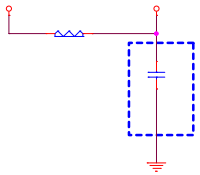

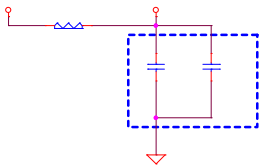

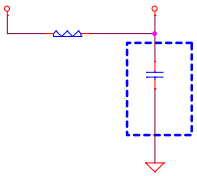

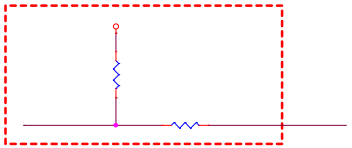

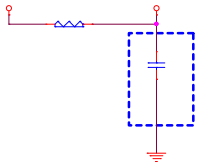

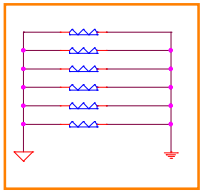

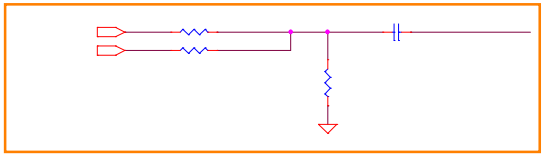

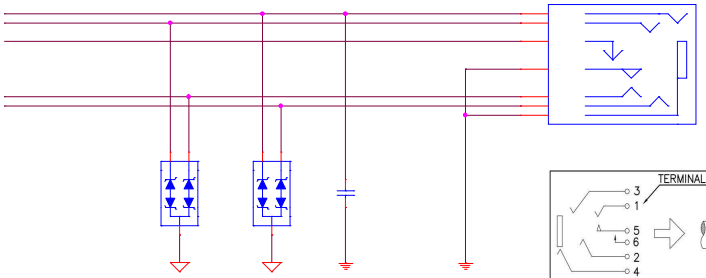
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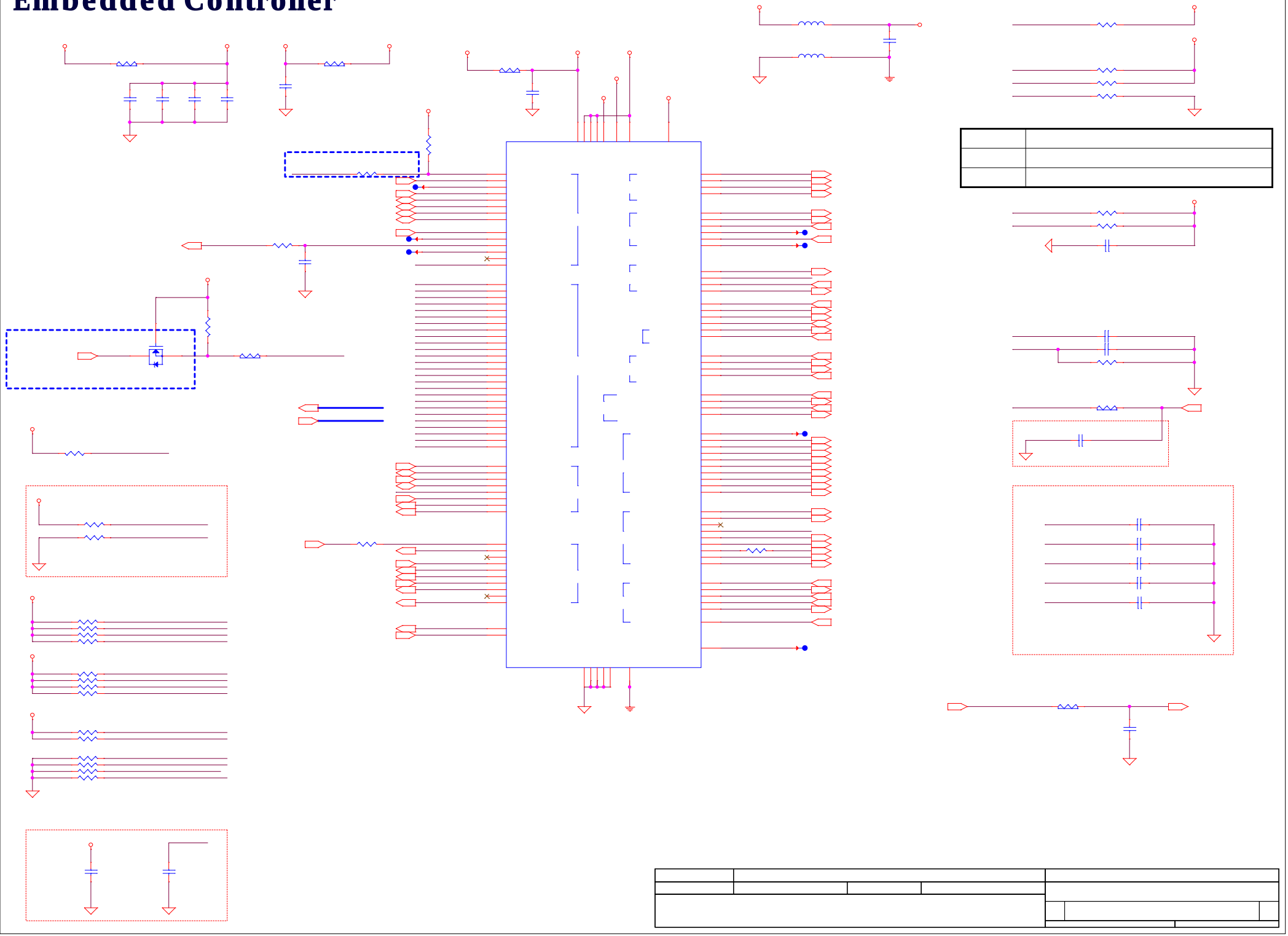

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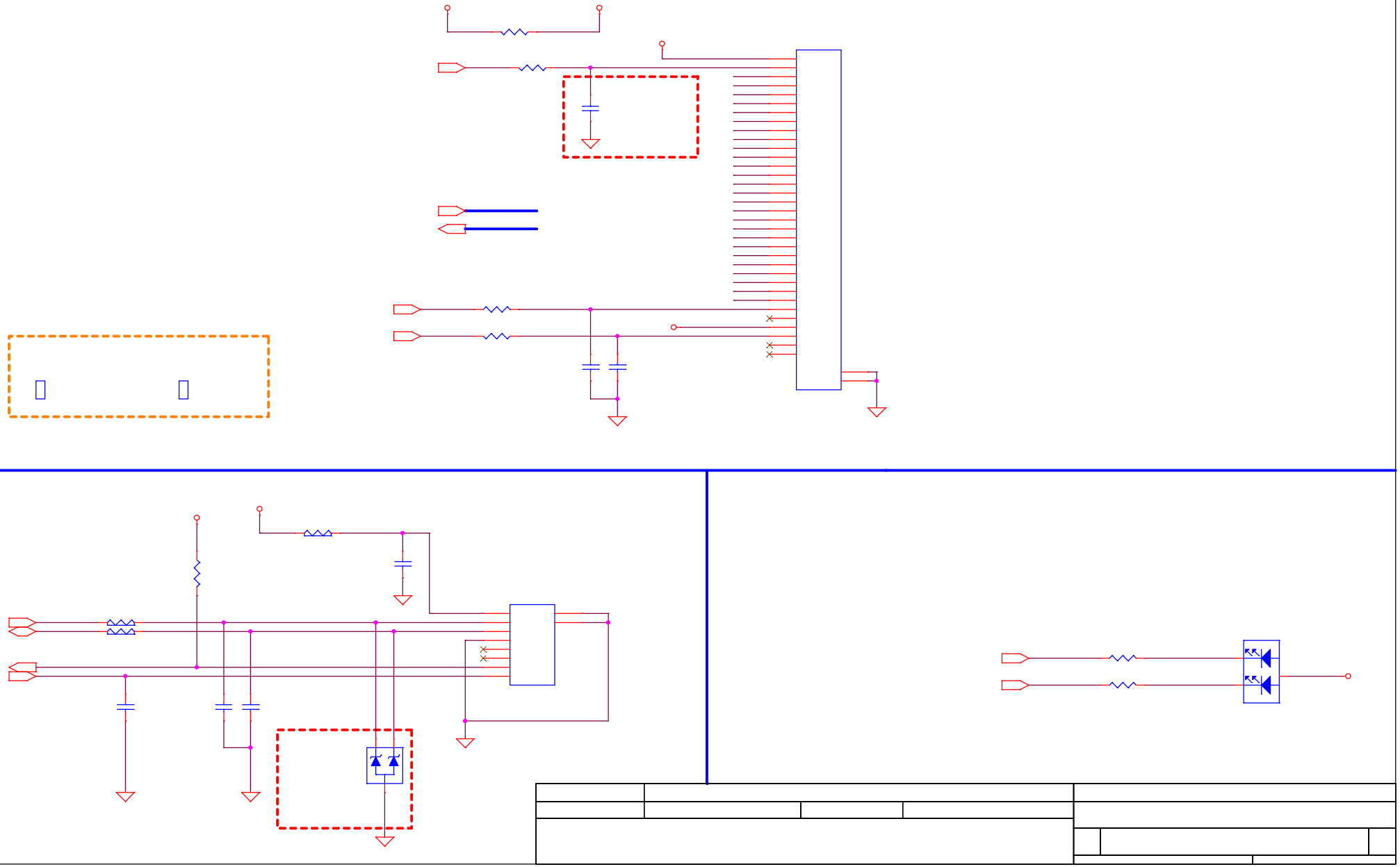

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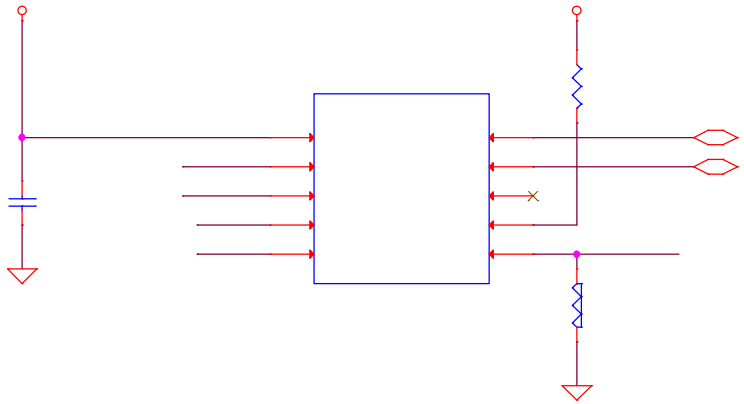

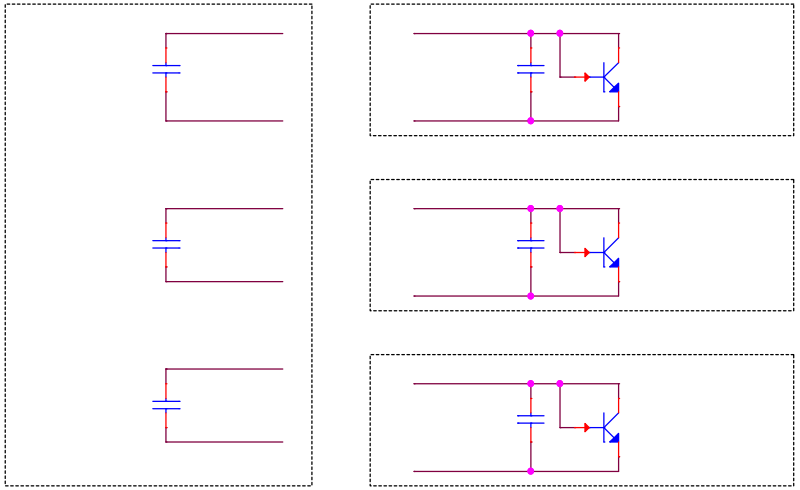

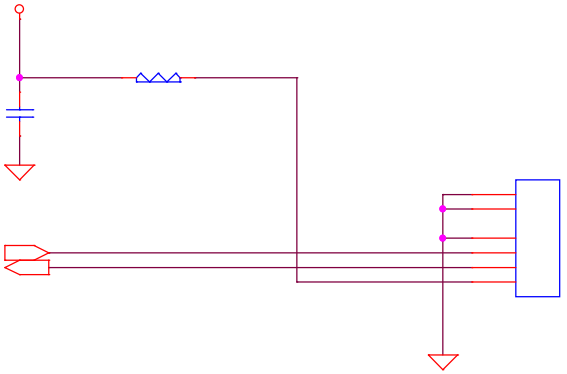

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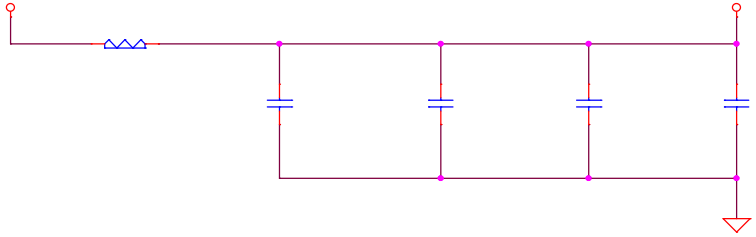
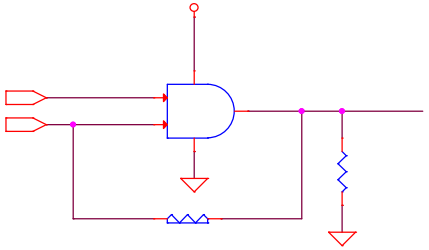
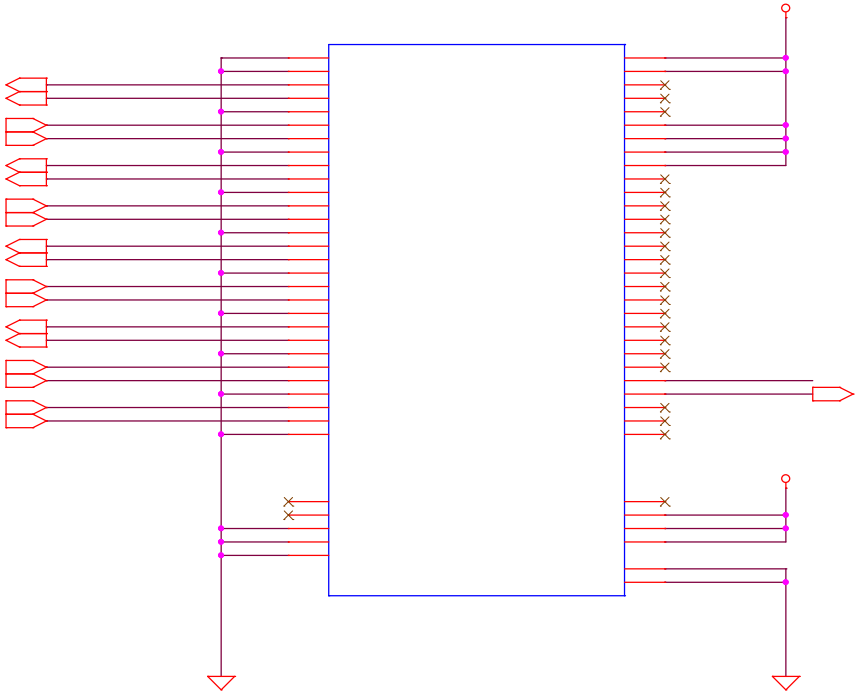
NGFF - SSD CONNECTOR (KEY-M)







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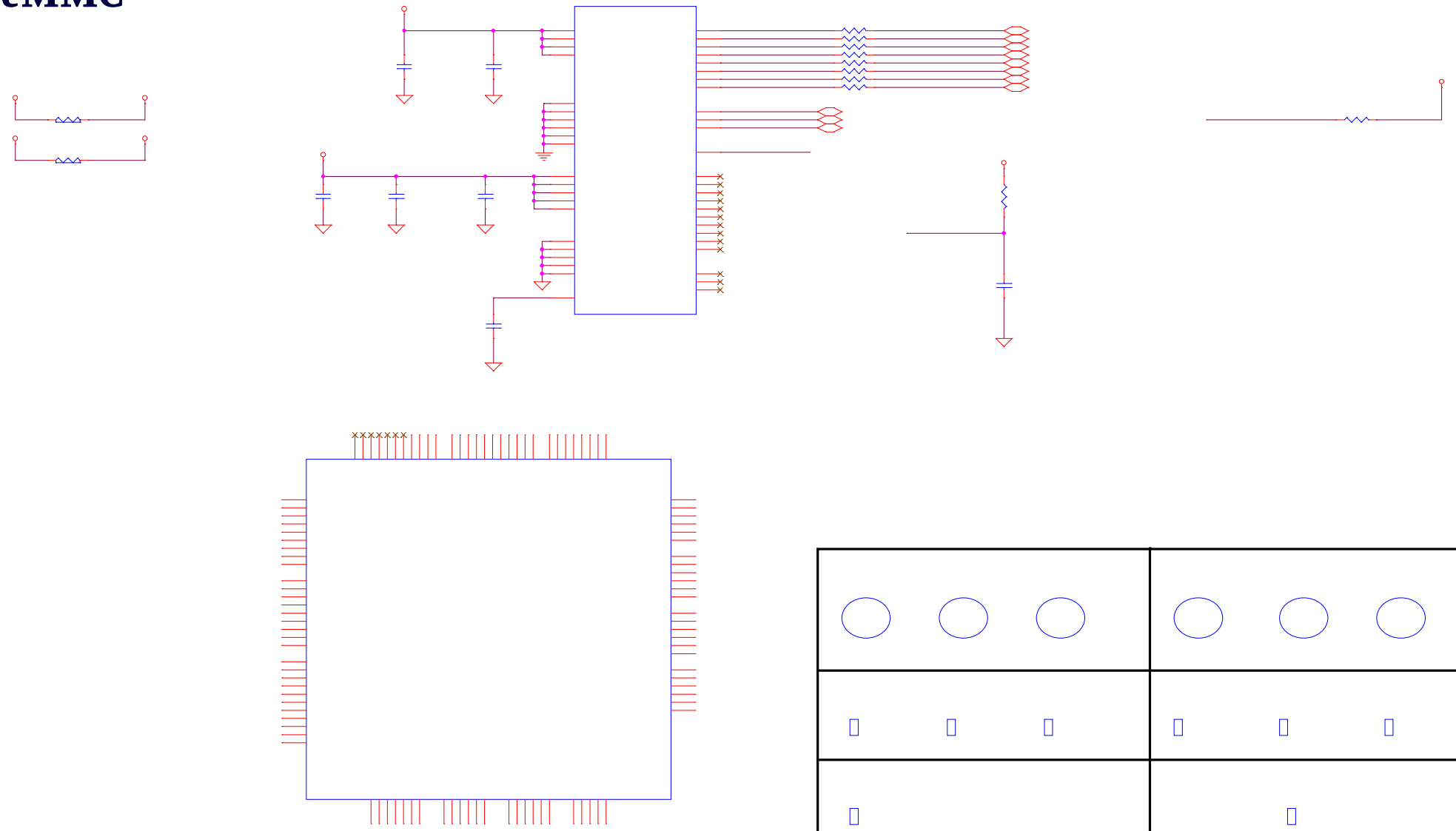

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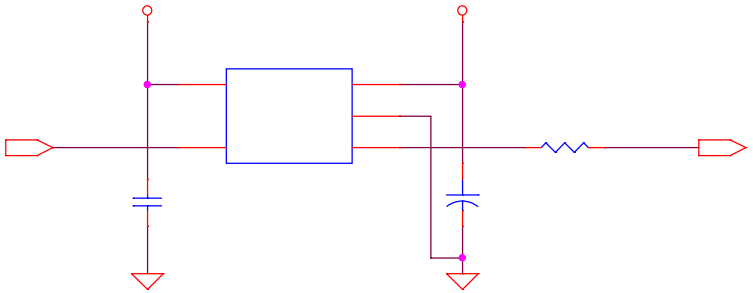
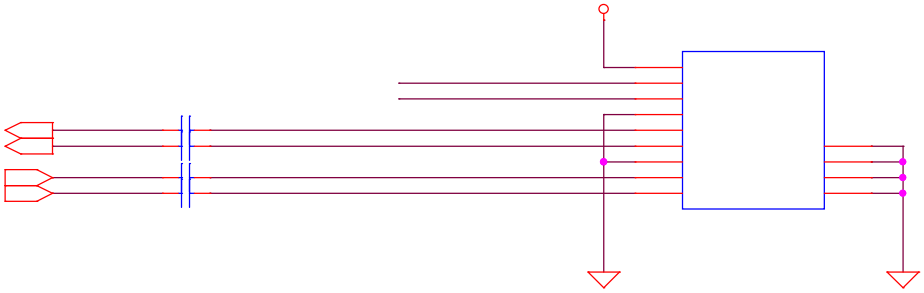
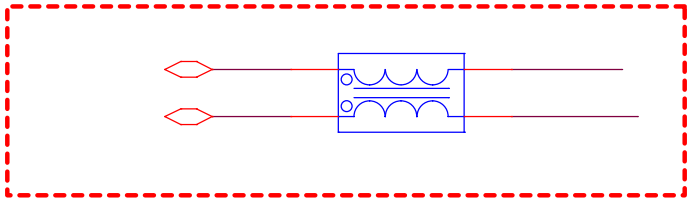
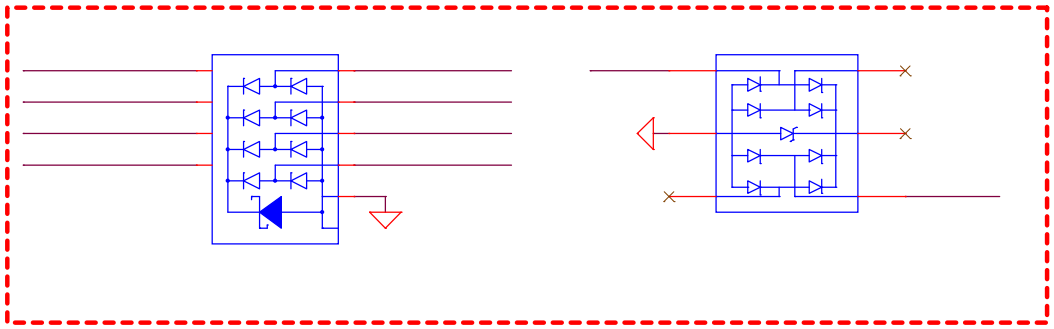
USB3.0_Port









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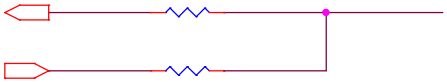

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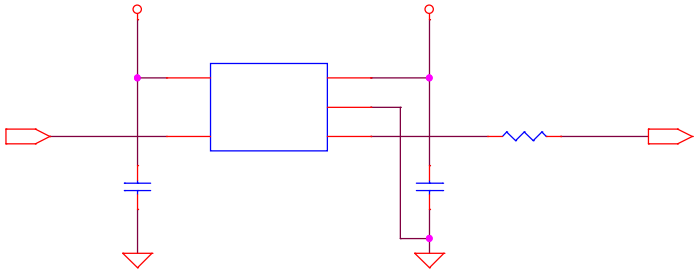

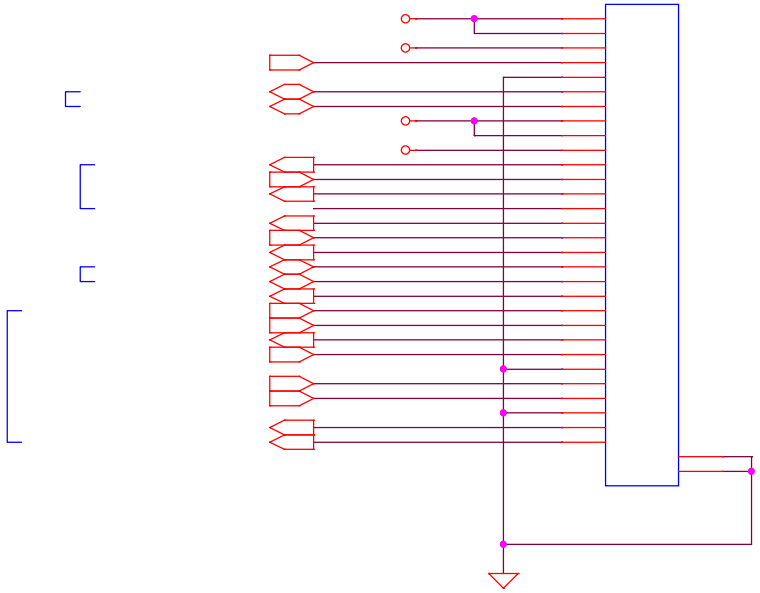

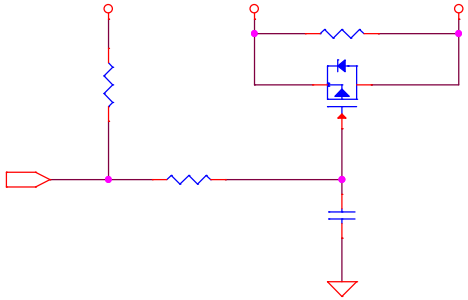

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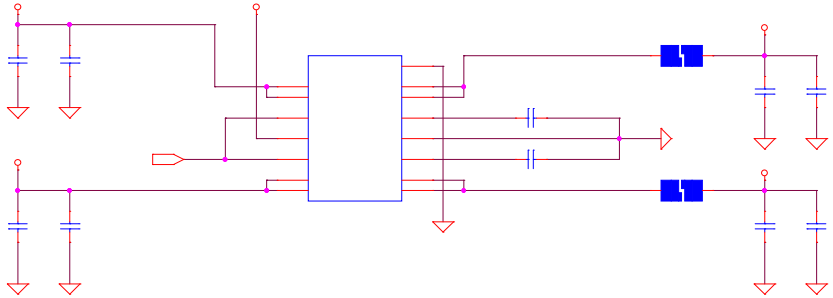

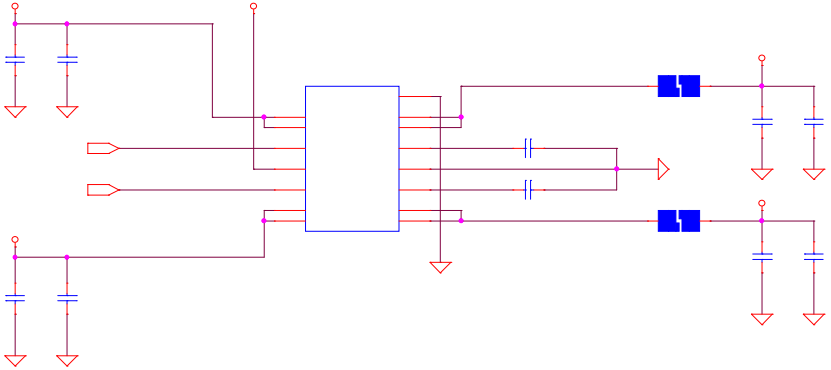

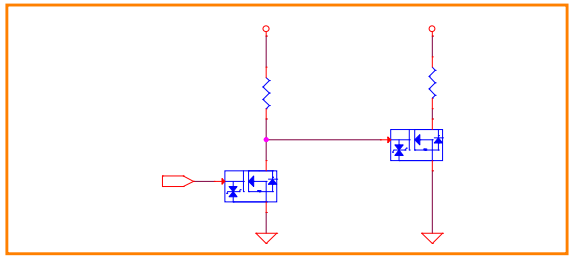

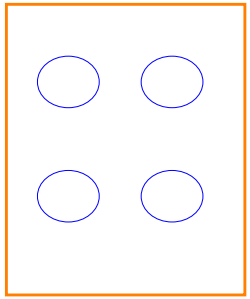

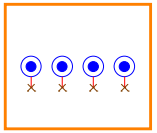

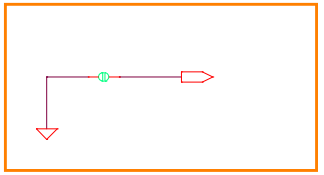

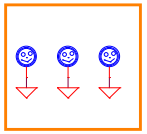

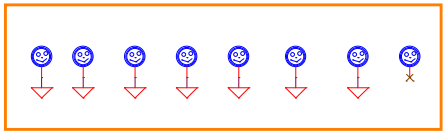

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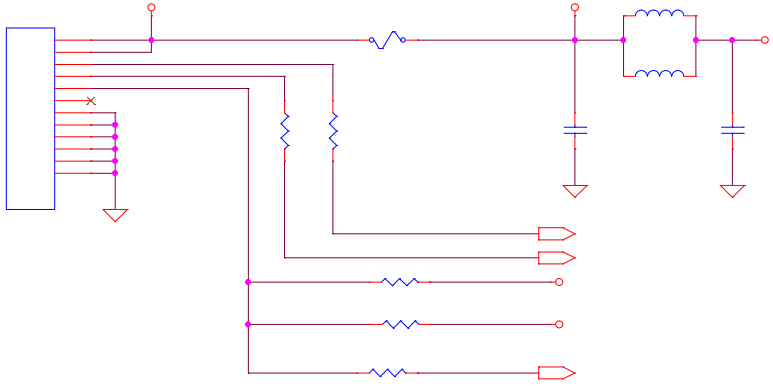

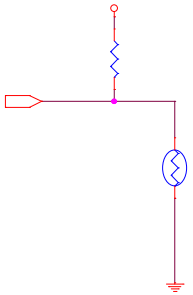

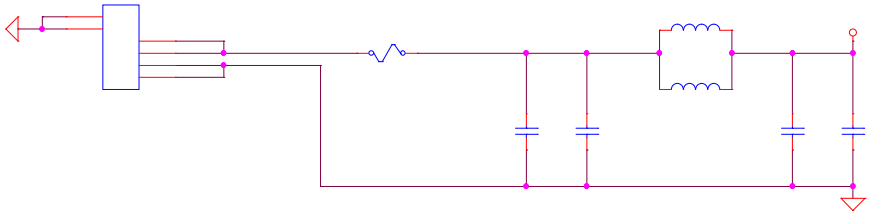

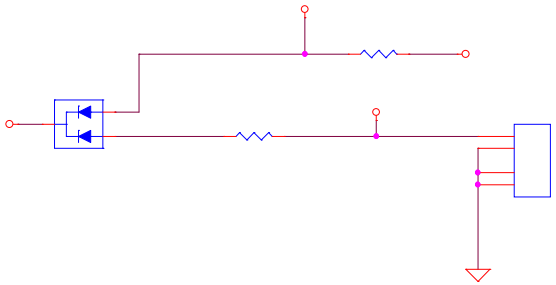

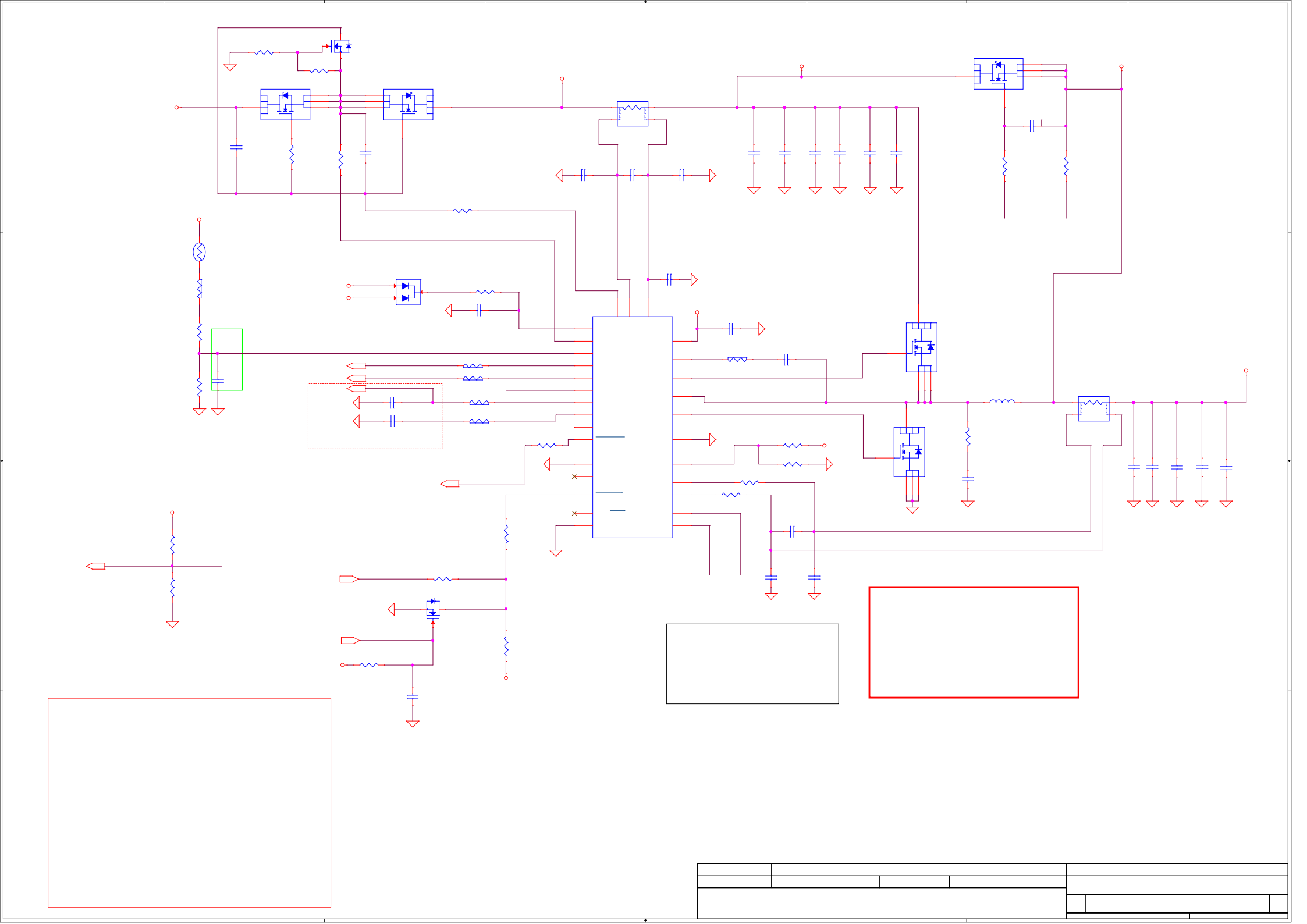

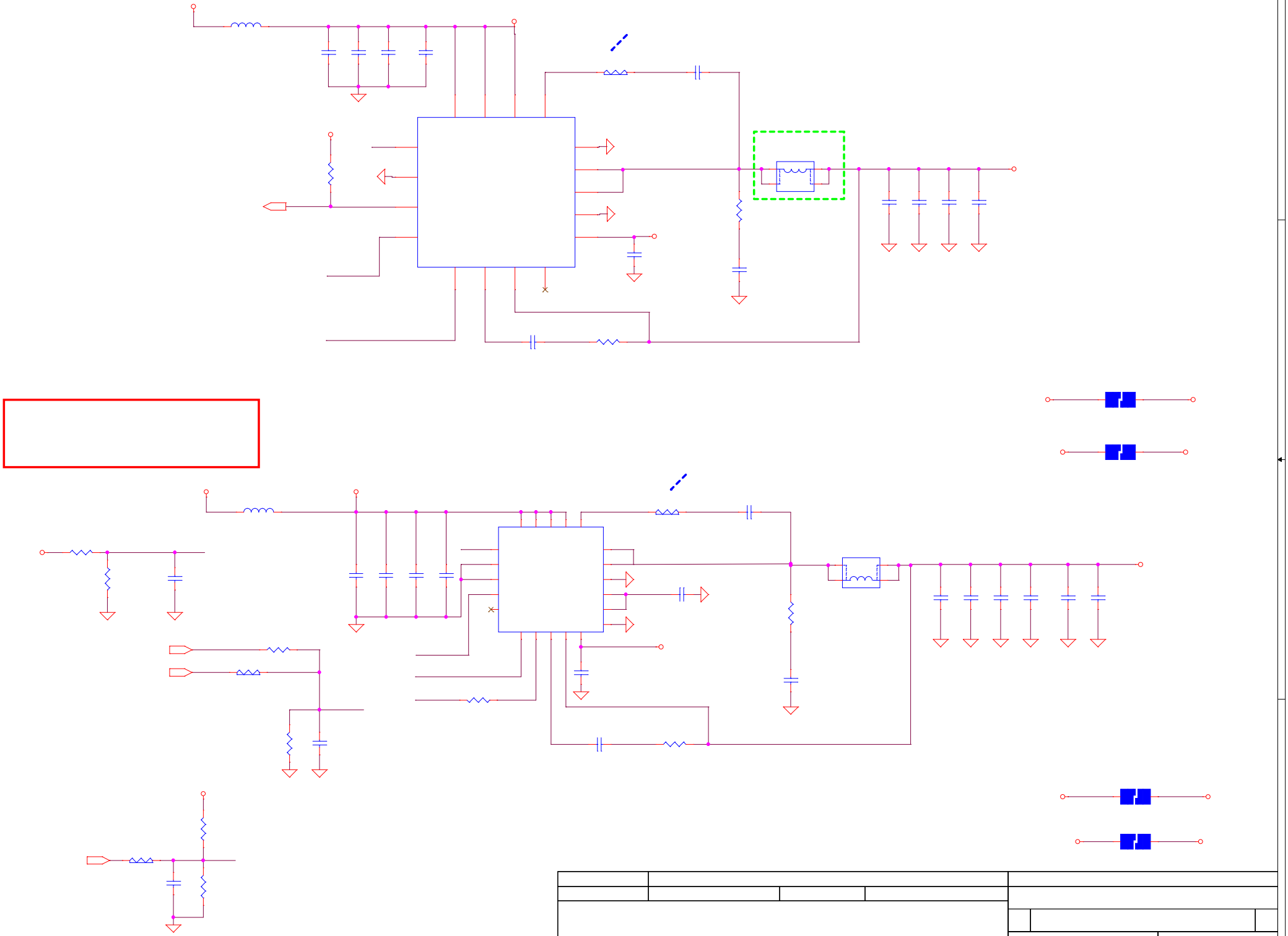

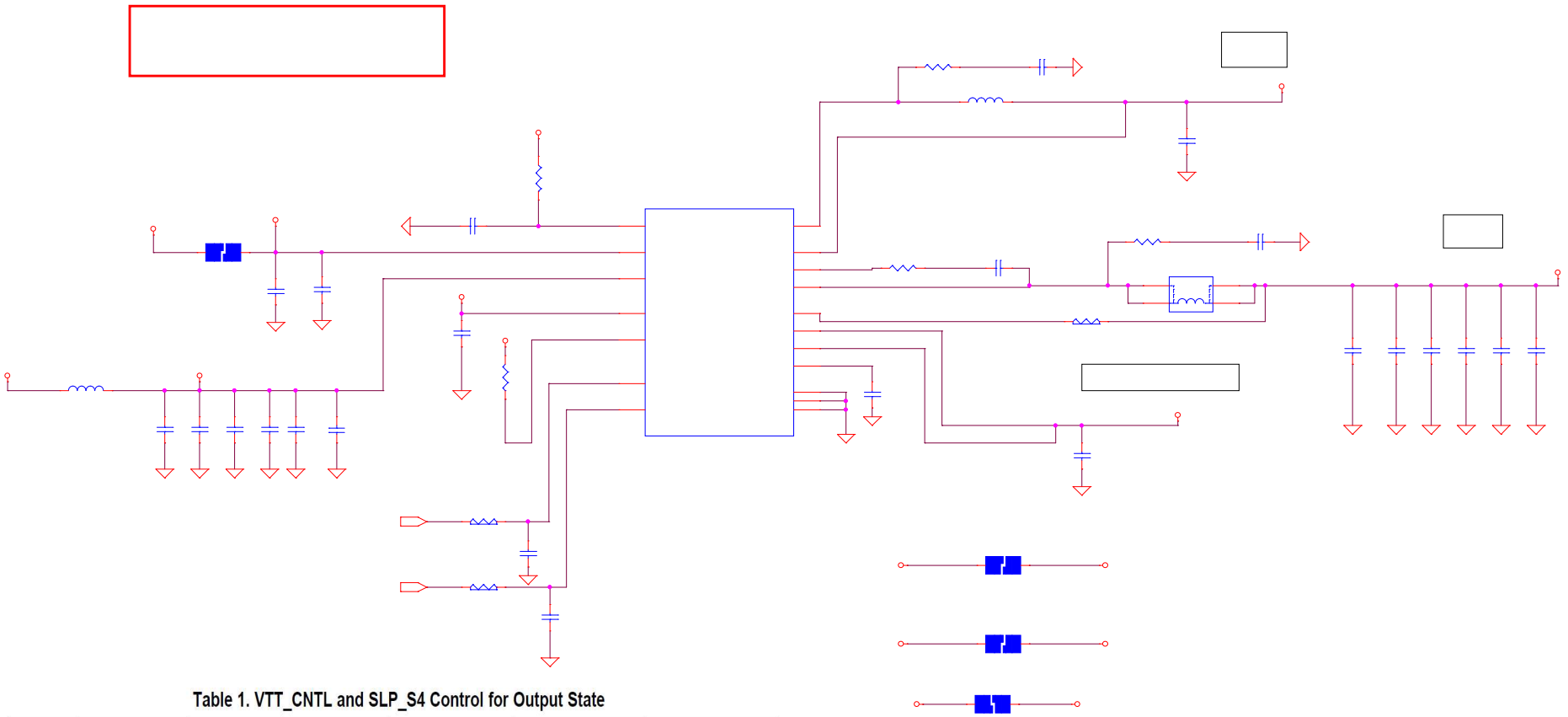

Table 1. VTT_CNTL and SLP_S4 Control for Output State

STATE	VTT_CNTL	SLP_S4	VPP	VDDQ	VTTREF	VTT
S0	HI	HI	ON	ON	ON	ON
S3	LO	HI	ON	ON	ON	OFF (High-Z)
S5/S4	LO	LO	OFF (discharge)	OFF (discharge)	OFF (discharge)	OFF (discharge)